Vincotech

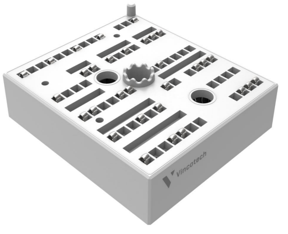
80-M212PNB015M7-K778C70
datasheet

MiniSKiiP® PIM 2 **1200 V / 15 A**

Features

- CI topology with splitted phase for switched reluctance motor applications
- IGBT M7 with low VCEsat and improved EMC behavior
- Solder-free spring contact technology
- Built-in NTC

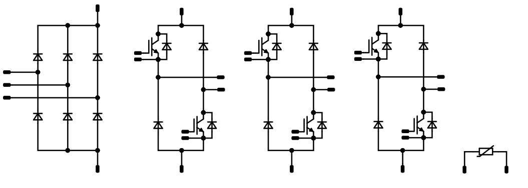
MiniSKiiP® 2 16 mm housing



Target applications

- Industrial Drives

Schematic



Types

- 80-M212PNB015M7-K778C70

Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
Inverter Switch					
Collector-emitter voltage	V_{CES}			1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$	T_s = 80 °C	26	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}		30	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	79	W
Gate-emitter voltage	V_{GES}			±20	V
Short circuit ratings	t_{SC}	V_{GE} = 0 V, V_{CC} = 800 V	T_j = 150 °C	9,5	µs
Maximum junction temperature	T_{jmax}			175	°C
Inverter Diode					
Peak repetitive reverse voltage	V_{RRM}			1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	27	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}		30	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	61	W
Maximum junction temperature	T_{jmax}			175	°C
Inverter Sw. Prot. Diode					
Peak repetitive reverse voltage	V_{RRM}			1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	15	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}		10	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	41	W
Maximum junction temperature	T_{jmax}			175	°C

Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
Rectifier Diode					
Peak repetitive reverse voltage	V_{RRM}			1600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	43	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, t_p = 10 ms	T_j = 150 °C	200	A
Surge current capability	I^2t			200	A^2s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	58	W
Maximum junction temperature	T_{jmax}			150	°C
Module Properties					
Thermal Properties					
Storage temperature	T_{stg}			-40…+125	°C
Operation temperature under switching condition	T_{jop}			-40…+(T_{jmax} - 25)	°C
Isolation Properties					
Isolation voltage	V_{isol}	DC Test Voltage*	t_p = 2 s	5500	V
Isolation voltage	V_{isol}	AC Voltage	t_p = 1 min	2500	V
Creepage distance		With std lid For more informations see handling instructions		6,3	mm
Clearance		With std lid For more informations see handling instructions		6,3	mm
Comparative Tracking Index	CTI			≥ 200	

*100 % tested in production

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Inverter Switch										
Static										
Gate-emitter threshold voltage	$V_{GE(th)}$			10	0,0015	25	5,4	6	6,6	V
Collector-emitter saturation voltage	V_{CEsat}		15		15	25 125 150		1,7 1,95 2,01	2,15(1)	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			60	µA
Gate-emitter leakage current	I_{GES}		0	0		25			500	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}							2900		pF
Output capacitance	C_{oes}		0	10		25		120		pF
Reverse transfer capacitance	C_{res}							34		pF
Gate charge	Q_g	V_{CC} = 600 V	15		15	25		110		nC
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 2,5 W/mK (HPTP)						1,2		K/W
Dynamic										
Turn-on delay time	$t_{d(on)}$					25 125 150		104,96 102,72 103,36		ns
Rise time	t_r	R_{gon} = 16 Ω R_{goff} = 16 Ω				25 125 150		27,2 29,44 29,76		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		156,8 182,08 187,84		ns
Fall time	t_f		±15	600	15	25 125 150		109,01 132,9 138,82		ns
Turn-on energy (per pulse)	E_{on}	Q_{rFWD}=1,63 µC Q_{rFWD}=2,64 µC Q_{rFWD}=2,98 µC				25 125 150		1,23 1,59 1,7		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		1,08 1,47 1,57		mWs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Inverter Diode										
Static										
Forward voltage	V_F				15	25 125 150		1,62 1,74 1,73	2,1(1)	V
Reverse leakage current	I_R	V_r = 1200 V				25			30	µA
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 2,5 W/mK (HPTP)						1,55		K/W
Dynamic										
Peak recovery current	I_{RRM}	di/dt=472 A/µs di/dt=427 A/µs di/dt=418 A/µs	±15	600	15	25 125 150		16,06 17,38 17,69		A
Reverse recovery time	t_{rr}					25 125 150		215,69 335,87 378,96		ns
Recovered charge	Q_r					25 125 150		1,63 2,64 2,98		µC
Reverse recovered energy	E_{rec}					25 125 150		0,557 0,986 1,14		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		242,84 134,86 125,11		A/µs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Inverter Sw. Prot. Diode										
Static										
Forward voltage	V_F				5	25 125 150		1,57 1,65 1,65	2,1(1)	V
Reverse leakage current	I_R	V_r = 1200 V				25			20	µA
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 2,5 W/mK (HPTP)						2,31		K/W
Rectifier Diode										
Static										
Forward voltage	V_F				18	25 125 150		1,12 1,03 1,02	1,5(1)	V
Reverse leakage current	I_R	V_r = 1600 V				25 150			100 1000	µA
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 2,5 W/mK (HPTP)						1,2		K/W

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Thermistor										
Static										
Rated resistance	R					25		5		kΩ
Deviation of R_{100}	$\Delta_{R/R}$	R_{100} = 493 Ω				100	-5		5	%
Power dissipation	P							245		mW
Power dissipation constant	d					25		1,4		mW/K
B-value	$B_{(25/50)}$	Tol. ±2 %						3375		K
B-value	$B_{(25/100)}$	Tol. ±2 %						3437		K
Vincotech Thermistor Reference									K	

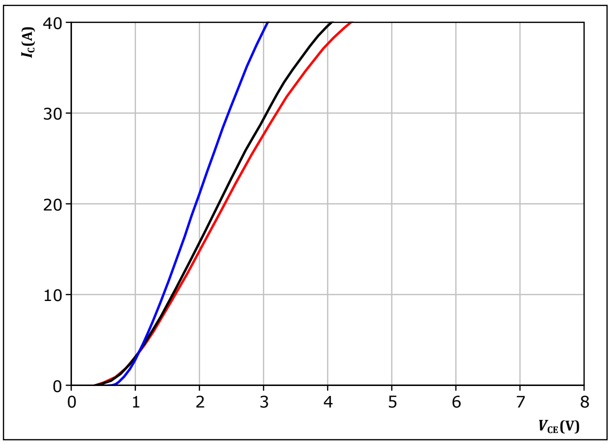
(1) Value at chip level
(2) Only valid with pre-applied Vincotech thermal interface material.

Inverter Switch Characteristics

figure 1. **IGBT**

Typical output characteristics

I_C = f(V_{CE})

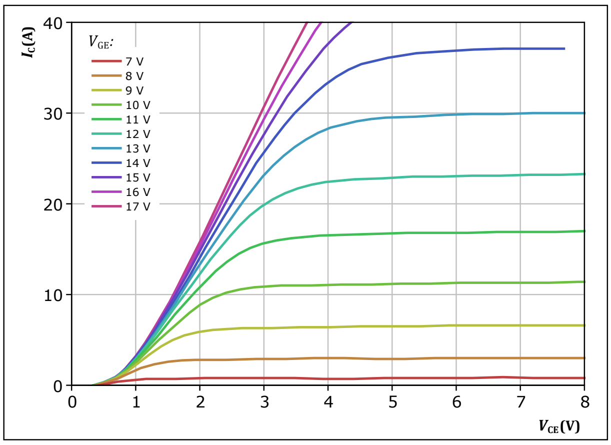


t_p =	250	µs		25 °C
V_{GE} =	15	V	T_j:	125 °C
				150 °C

figure 2. **IGBT**

Typical output characteristics

I_C = f(V_{CE})



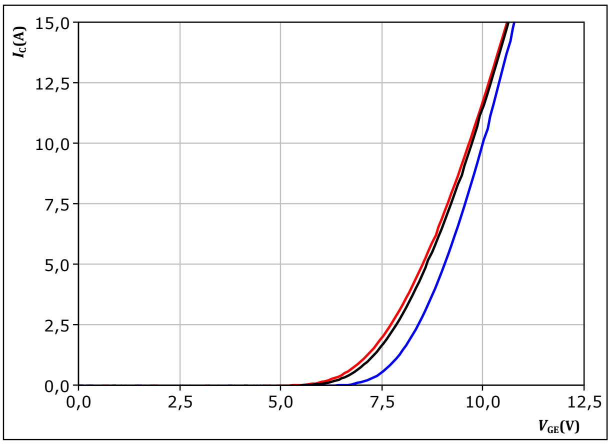
t_p =	250	µs
T_j =	150	°C

V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. **IGBT**

Typical transfer characteristics

I_C = f(V_{GE})

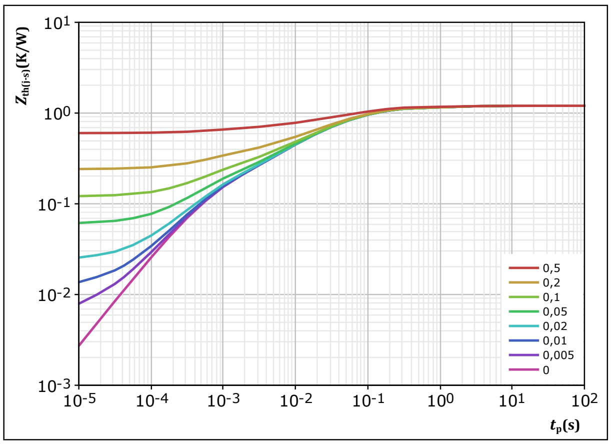


t_p =	250	µs		25 °C
V_{CE} =	10	V	T_j:	125 °C
				150 °C

figure 4. **IGBT**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D =	t_p / T	
$R_{th(j-s)}$ =	1,203	K/W

IGBT thermal model values

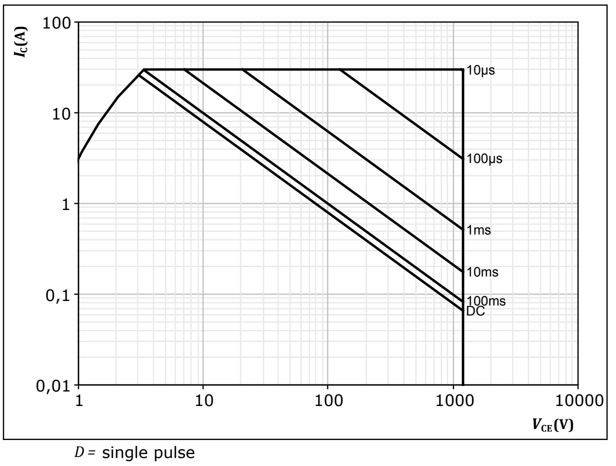
R (K/W)	τ (s)
8,45E-02	1,63E+00
4,66E-01	9,37E-02
3,90E-01	2,02E-02
1,45E-01	3,96E-03
1,16E-01	5,44E-04

Inverter Switch Characteristics

figure 5.	**IGBT**

Safe operating area

I_C = f(V_{CE})



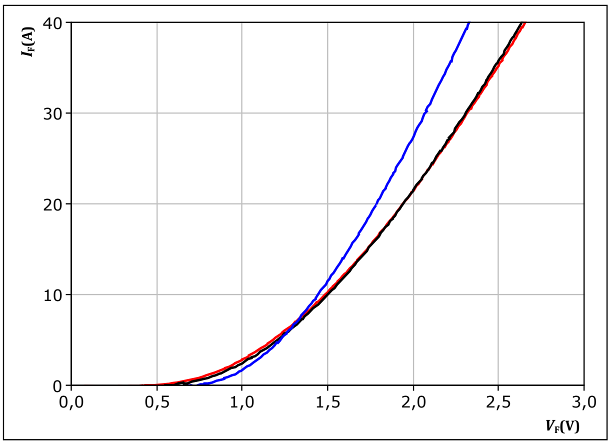
D =	single pulse	
T_s =	80	°C
V_{GE} =	15	V
T_j =	T_{jmax}	

Inverter Diode Characteristics

figure 6. **FWD**

Typical forward characteristics

$I_F = f(V_F)$



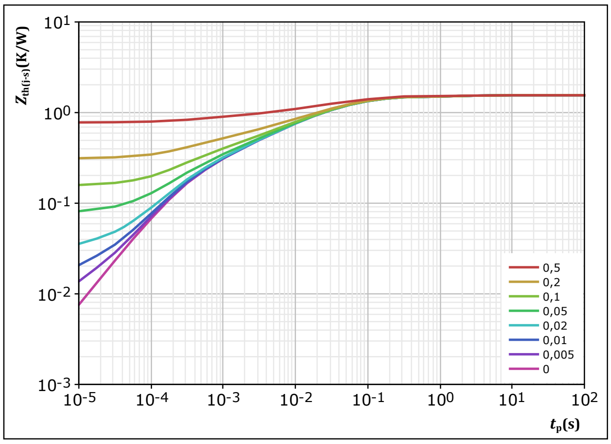
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 7. **FWD**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)} = f(t_p)$



D = t_p / T

$R_{th(j-s)}$ = 1,553 K/W

FWD thermal model values

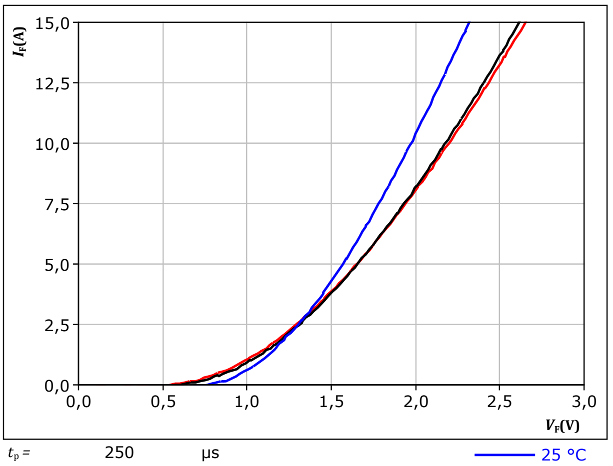
R (K/W)	τ (s)
8,50E-02	1,79E+00
5,68E-01	6,89E-02
4,67E-01	1,30E-02
2,46E-01	2,26E-03
1,87E-01	3,04E-04

Inverter Sw. Prot. Diode Characteristics

figure 8. FWD

Typical forward characteristics

$I_F = f(V_F)$



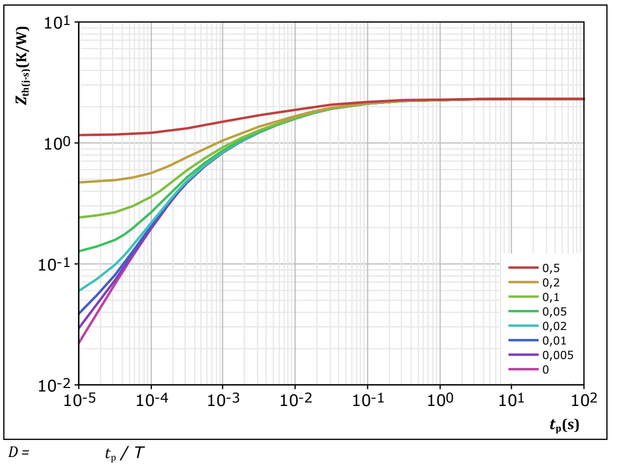
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 9. FWD

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)} = f(t_p)$



D = t_p / T

$R_{th(j-s)}$ = 2,313 K/W

FWD thermal model values

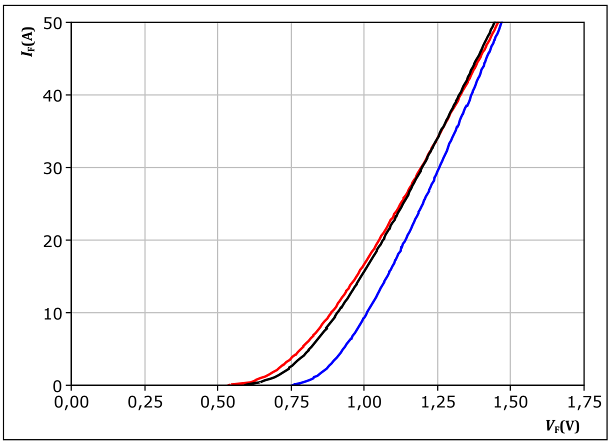
R (K/W)	τ (s)
1,03E-01	1,46E+00
4,16E-01	7,07E-02
7,17E-01	9,80E-03
6,03E-01	1,47E-03
4,75E-01	2,69E-04

Rectifier Diode Characteristics

figure 10. **Rectifier**

Typical forward characteristics

$I_F = f(V_F)$



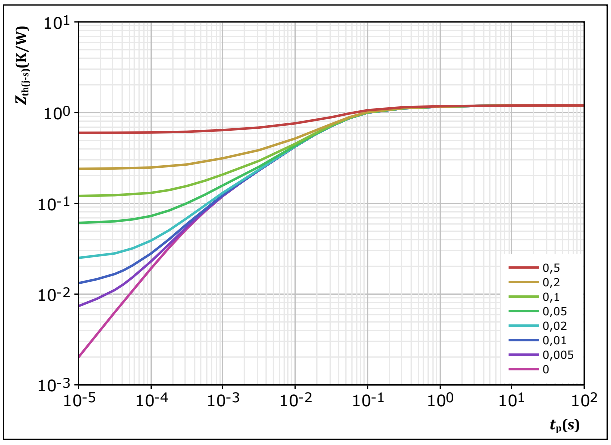
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 11. **Rectifier**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)} = f(t_p)$



$D = t_p / T$

$R_{th(j-s)}$ = 1,199 K/W

Rectifier thermal model values

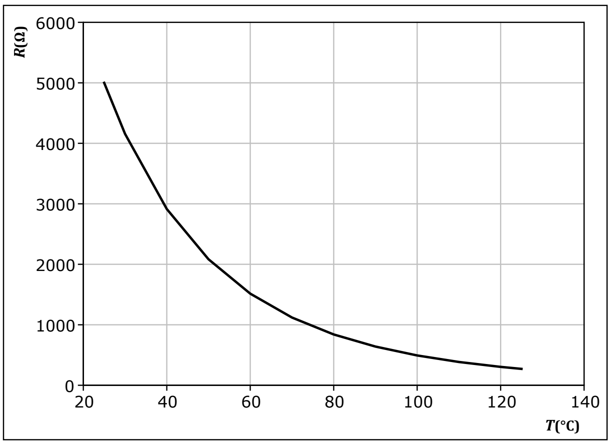
R (K/W)	τ (s)
4,23E-02	2,73E+00
1,04E-01	3,60E-01
5,86E-01	4,98E-02
2,59E-01	1,70E-02
1,36E-01	3,63E-03
7,19E-02	5,18E-04

Thermistor Characteristics

figure 12. **Thermistor**

Typical NTC characteristic as function of temperature

R_T = f(T)

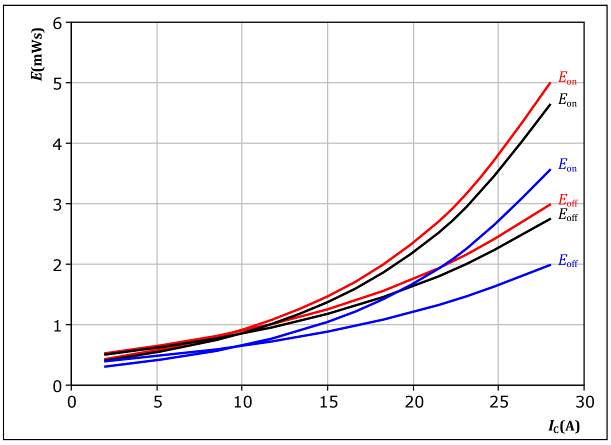


Inverter Switching Characteristics

figure 13. **IGBT**

Typical switching energy losses as a function of collector current

E = f(I_C)



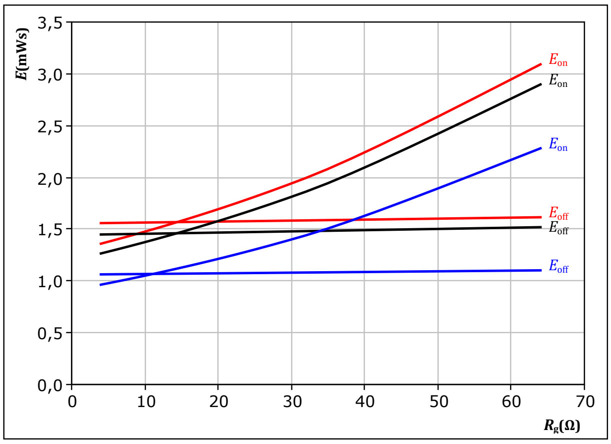
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	16	Ω		150 °C
R_{goff} =	16	Ω		

figure 14. **IGBT**

Typical switching energy losses as a function of gate resistor

E = f(R_g)



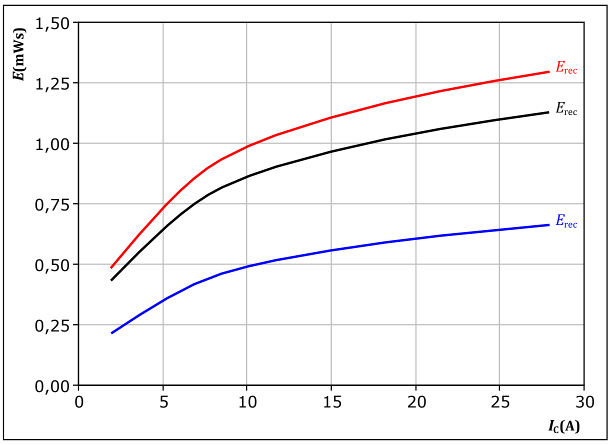
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	15	A		150 °C

figure 15. **FWD**

Typical reverse recovered energy loss as a function of collector current

E_{rec} = f(I_C)



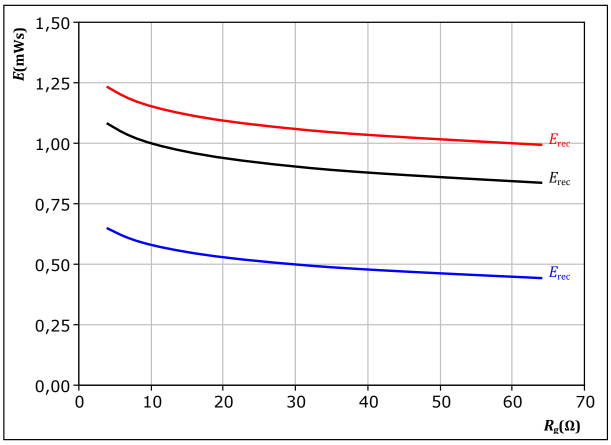
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	16	Ω		150 °C

figure 16. **FWD**

Typical reverse recovered energy loss as a function of gate resistor

E_{rec} = f(R_g)



With an inductive load at

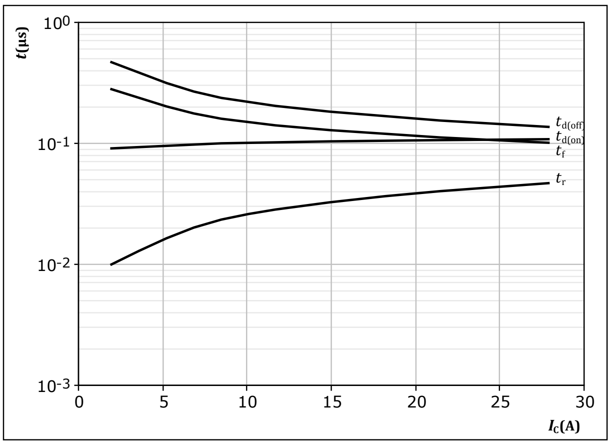
V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	15	A		150 °C

Inverter Switching Characteristics

figure 17. **IGBT**

Typical switching times as a function of collector current

t = f(I_C)



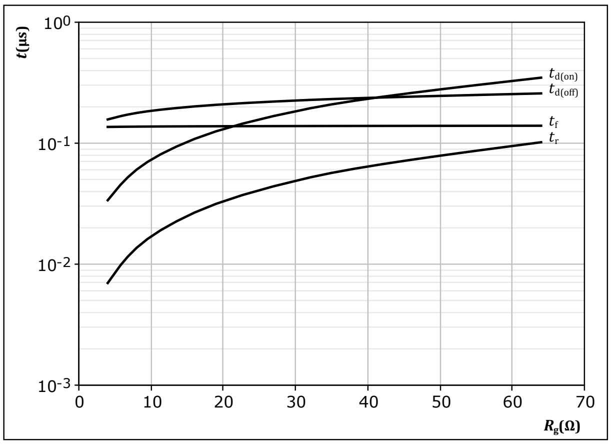
With an inductive load at

T_j =	150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	16	Ω
R_{goff} =	16	Ω

figure 18. **IGBT**

Typical switching times as a function of gate resistor

t = f(R_g)



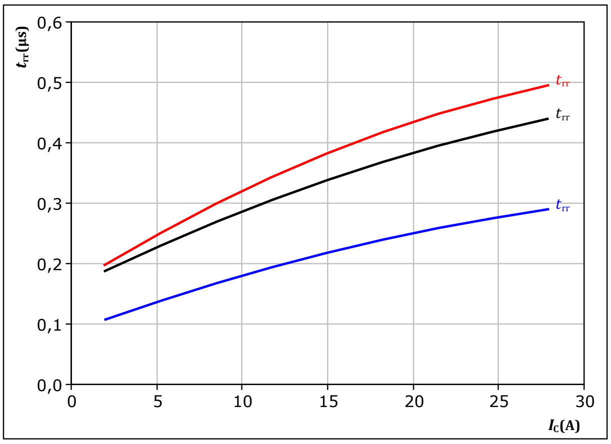
With an inductive load at

T_j =	150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
I_C =	15	A

figure 19. **FWD**

Typical reverse recovery time as a function of collector current

t_{rr} = f(I_C)



With an inductive load at

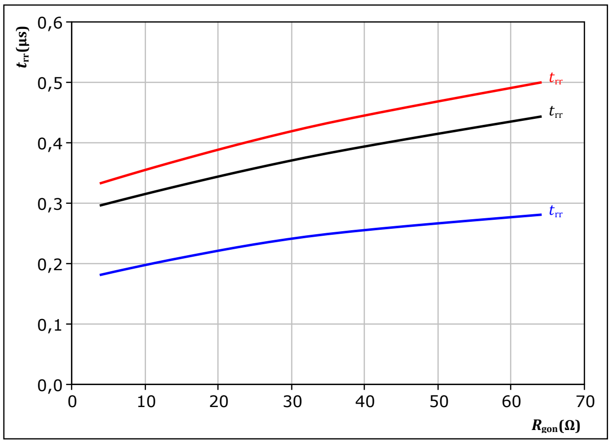
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	16	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 20. **FWD**

Typical reverse recovery time as a function of IGBT turn on gate resistor

t_{rr} = f(R_{gon})



With an inductive load at

V_{CE} =	600	V
V_{GE} =	±15	V
I_C =	15	A

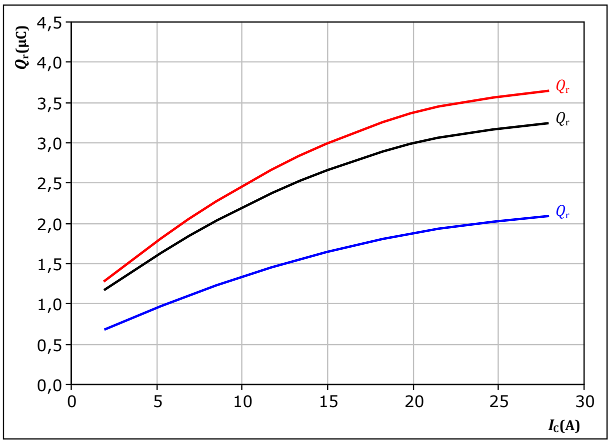
T_j: 25 °C, 125 °C, 150 °C

Inverter Switching Characteristics

figure 21. **FWD**

Typical recovered charge as a function of collector current

Q_r = f(I_C)



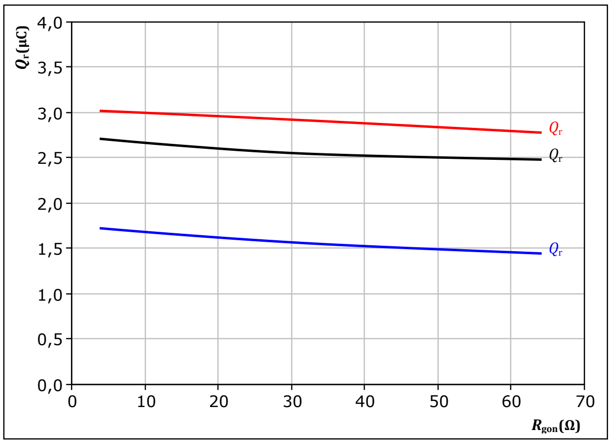
With an inductive load at

V_{CE} =	600	V	T_j: 25 °C / 125 °C / 150 °C
V_{GE} =	±15	V	
R_{gon} =	16	Ω	

figure 22. **FWD**

Typical recovered charge as a function of turn on gate resistor

Q_r = f(R_{gon})



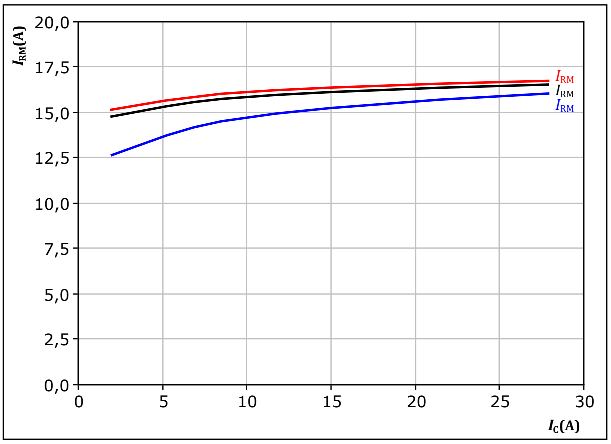
With an inductive load at

V_{CE} =	600	V	T_j: 25 °C / 125 °C / 150 °C
V_{GE} =	±15	V	
I_C =	15	A	

figure 23. **FWD**

Typical peak reverse recovery current as a function of collector current

I_{RM} = f(I_C)



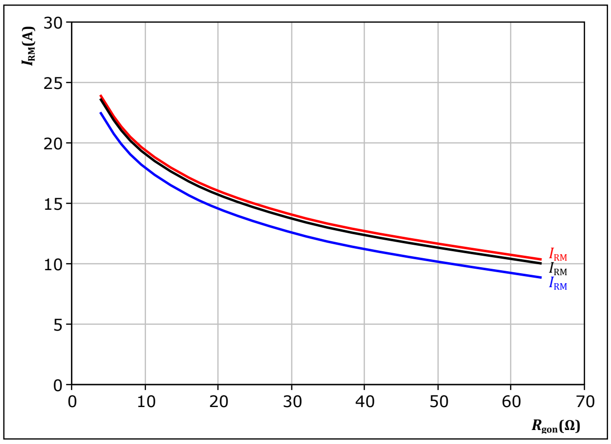
With an inductive load at

V_{CE} =	600	V	T_j: 25 °C / 125 °C / 150 °C
V_{GE} =	±15	V	
R_{gon} =	16	Ω	

figure 24. **FWD**

Typical peak reverse recovery current as a function of turn on gate resistor

I_{RM} = f(R_{gon})



With an inductive load at

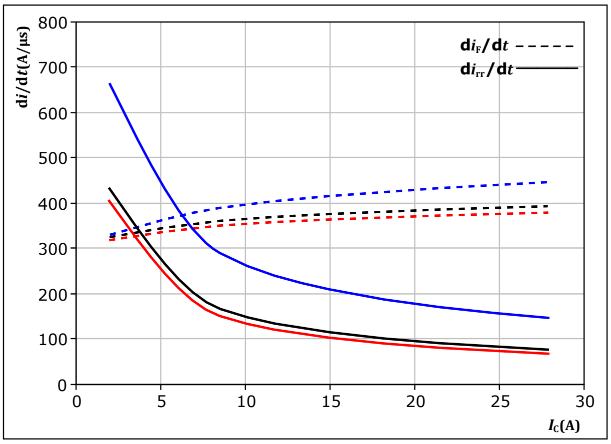
V_{CE} =	600	V	T_j: 25 °C / 125 °C / 150 °C
V_{GE} =	±15	V	
I_C =	15	A	

Inverter Switching Characteristics

figure 25. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of collector current

di_F/dt, di_{rr}/dt = f(I_C)



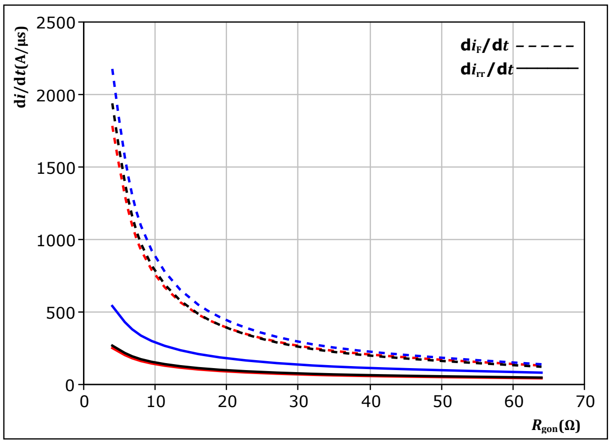
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	16	Ω		150 °C

figure 26. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor

di_F/dt, di_{rr}/dt = f(R_{gon})



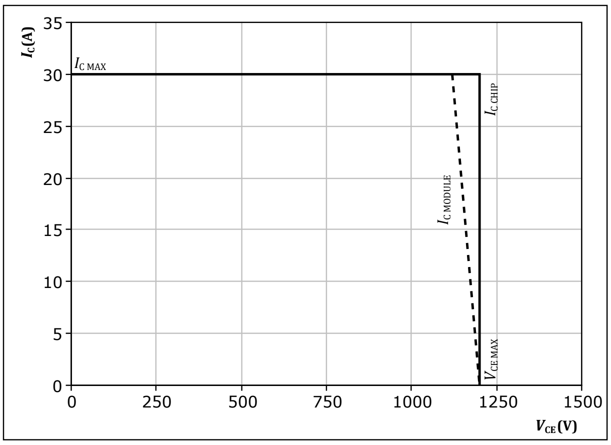
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	15	A		150 °C

figure 27. **IGBT**

Reverse bias safe operating area

I_C = f(V_{CE})



At	T_j =	150	°C
	R_{gon} =	16	Ω
	R_{goff} =	16	Ω

Inverter Switching Definitions

figure 28. **IGBT**

Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff} (t_{tEoff} = integrating time for E_{off})

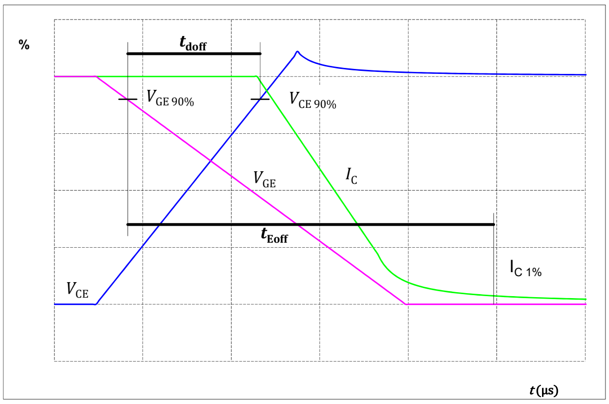


figure 29. **IGBT**

Turn-on Switching Waveforms & definition of t_{don}, t_{Eon} (t_{Eon} = integrating time for E_{on})

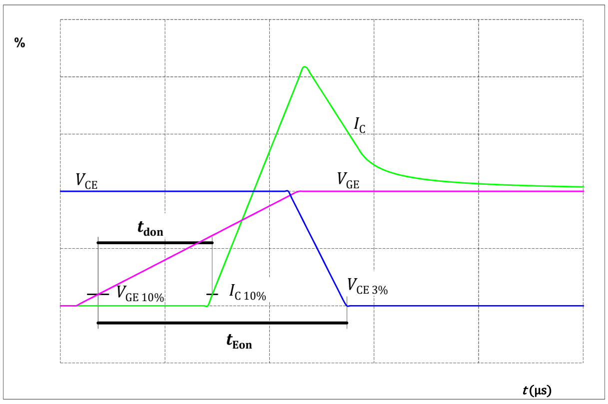


figure 30. **IGBT**

Turn-off Switching Waveforms & definition of t_f

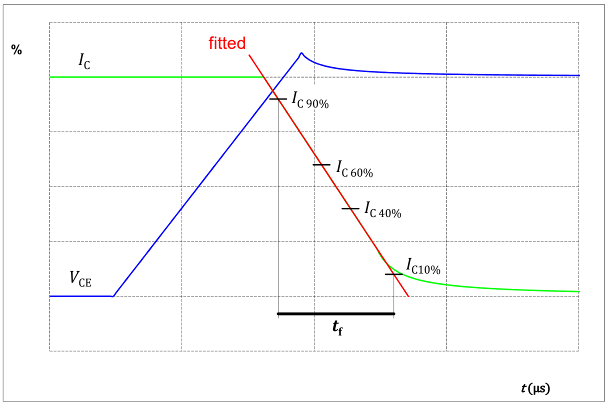
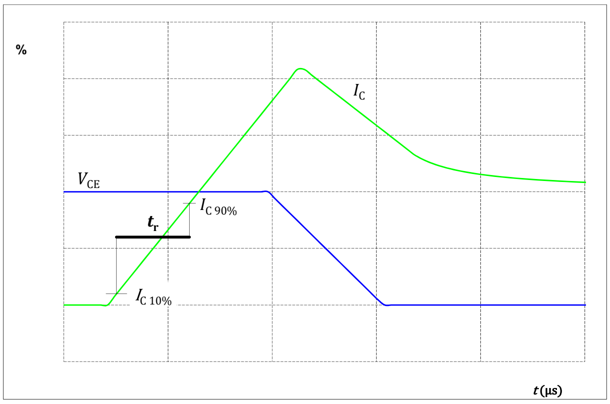


figure 31. **IGBT**

Turn-on Switching Waveforms & definition of t_r



Inverter Switching Definitions

figure 32. **FWD**

Turn-off Switching Waveforms & definition of t_{rr}

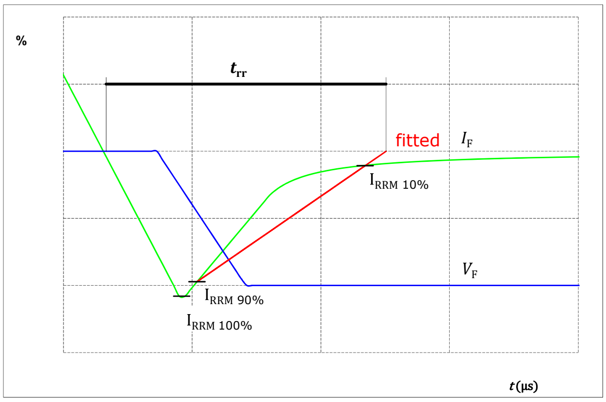
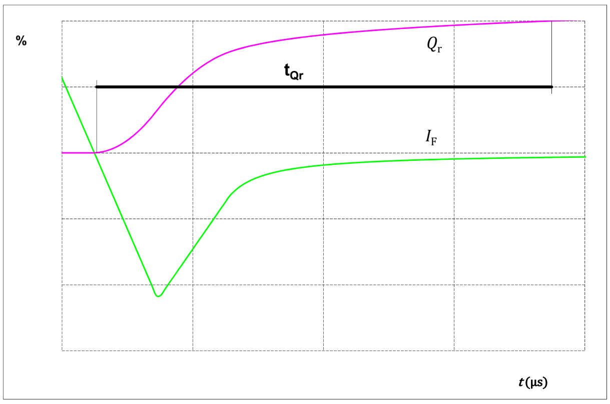


figure 33. **FWD**

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)



80-M212PNB015M7-K778C70
datasheet

Vincotech

Ordering Code

Version	Ordering Code
With std lid (6.5mm height) + no thermal grease	80-M212PNB015M7-K778C70-/0A/
With thin lid (2.8mm height) + no thermal grease	80-M212PNB015M7-K778C70-/0B/
With std lid (6.5mm height) + thermal grease (0,8 W/mK, P12, silicone-based)	80-M212PNB015M7-K778C70-/1A/
With thin lid (2.8mm height) + thermal grease (0,8 W/mK, P12, silicone-based)	80-M212PNB015M7-K778C70-/1B/
With std lid (6.5mm height) + thermal grease (2,5 W/mK, TG20032, silicone-free)	80-M212PNB015M7-K778C70-/4A/
With thin lid (2.8mm height) + thermal grease (2,5 W/mK, TG20032, silicone-free)	80-M212PNB015M7-K778C70-/4B/
With std lid (6.5mm height) + thermal grease (2,5 W/mK, HPTP, silicone-based)	80-M212PNB015M7-K778C70-/5A/
With thin lid (2.8mm height) + thermal grease (2,5 W/mK, HPTP, silicone-based)	80-M212PNB015M7-K778C70-/5B/

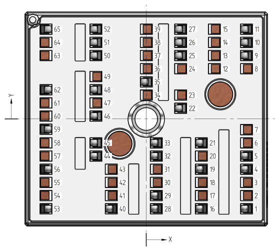
Marking

	Name		Date code	UL & VIN	Lot	Serial
Text	NN-NNNNNNNNNNNNNN-TTTTTTVV		WWYY	UL VIN	LLLLL	SSSS
Datamatrix	Type&Ver	Lot number	Serial	Date code		
	TTTTTTTVV	LLLLL	SSSS	WWYY		

Outline

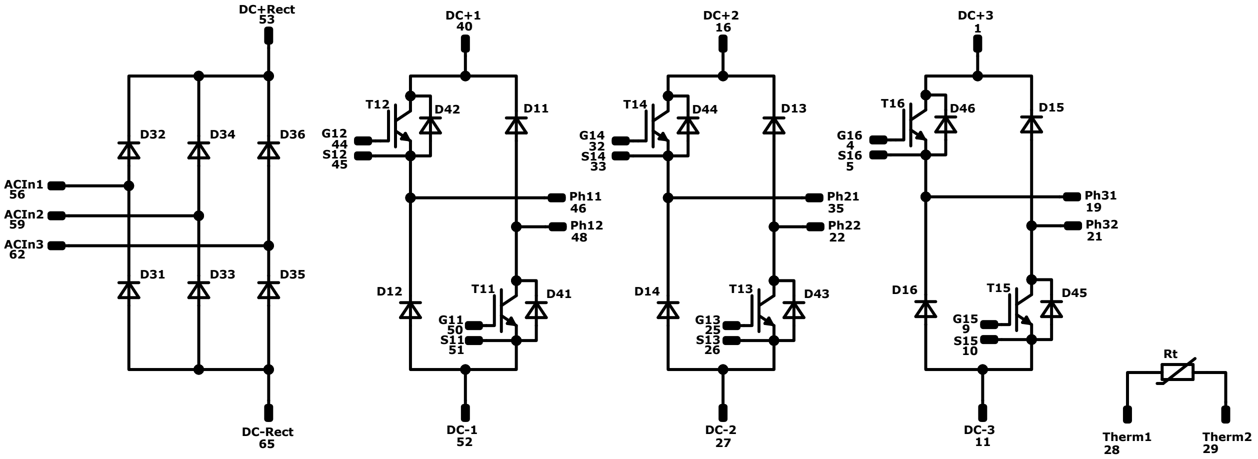
Pin table [mm]

Pin	X	Y	Function	34	not assembled		
1	24,38	-21,8	DC+3	35	0,03	9	Ph21
2	not assembled			36	not assembled		
3	not assembled			37	not assembled		
4	24,38	-12,2	G16	38	not assembled		
5	24,38	-9	S16	39	not assembled		
6	not assembled			40	-8,5	-21,8	DC+1
7	not assembled			41	not assembled		
8	not assembled			42	not assembled		
9	24,38	15,4	G15	43	not assembled		
10	24,38	18,6	S15	44	-12,22	-9	G12
11	24,38	21,8	DC-3	45	-12,22	-5,8	S12
12	not assembled			46	-12,22	0,7	Ph11
13	not assembled			47	not assembled		
14	not assembled			48	-12,22	7,1	Ph12
15	not assembled			49	not assembled		
16	13,42	-21,8	DC+2	50	-12,22	15,4	G11
17	not assembled			51	-12,22	18,6	S11
18	not assembled			52	-12,22	21,8	DC-1
19	13,42	-12,2	Ph31	53	-24,38	-21,8	DC+Rect
20	not assembled			54	not assembled		
21	13,42	-5,8	Ph32	55	not assembled		
22	8,38	2,6	Ph22	56	-24,38	-12,2	ACin1
23	not assembled			57	not assembled		
24	not assembled			58	not assembled		
25	8,38	15,4	G13	59	-24,38	-2,5	ACin2
26	8,38	18,6	S13	60	not assembled		
27	8,38	21,8	DC-2	61	not assembled		
28	2,46	-21,8	Therm1	62	-24,38	7,1	ACin3
29	2,46	-18,6	Therm2	63	not assembled		
30	not assembled			64	not assembled		
31	not assembled			65	-24,38	21,8	DC-Rect
32	2,46	-9	G14				
33	2,46	-5,8	S14				



Pad positions refers to center point. For more informations on pad design please see package data

Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14, T15, T16	IGBT	1200 V	15 A	Inverter Switch	
D11, D12, D13, D14, D15, D16	FWD	1200 V	15 A	Inverter Diode	
D41, D42, D43, D44, D45, D46	FWD	1200 V	5 A	Inverter Sw. Prot. Diode	
D31, D32, D33, D34, D35, D36	Rectifier	1600 V	18 A	Rectifier Diode	
Rt	Thermistor			Thermistor	

Packaging instruction				
Standard packaging quantity (SPQ) 72	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for MiniSKiiP® 2 packages see vincotech.com website.

Package data
Package data for MiniSKiiP® 2 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.

Document No.:	Date:	Modification:	Pages
80-M212PNB015M7-K778C70-D2-14	6 Apr. 2021	Correct Pinout and Outline	