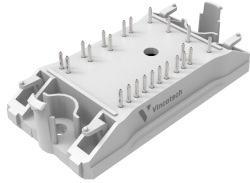
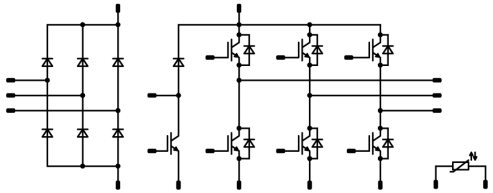




flowPIM 0		1200 V / 15 A	
Features		flow 0 12 mm housing	
<ul style="list-style-type: none">• 2 Clips housing in 12mm height• Trench Fieldstop Technology IGBT4• Enhanced Rectifier• Integrated Brake Chopper			
Target applications		Schematic	
<ul style="list-style-type: none">• Industrial Drives• Embedded Drives			
Types			
<ul style="list-style-type: none">• V23990-P840-A58-PM			



Vincotech

Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Inverter Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	22	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	45	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	65	W
Gate-emitter voltage	V_{GES}		± 20	V
Short circuit ratings	t_{SC}	$V_{GE} = 15\text{ V}$, $V_{CC} = 800\text{ V}$ $T_j = 150\text{ °C}$	10	μs
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$

Inverter Diode				
Peak repetitive reverse voltage	V_{RRM}		1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	25	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	30	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	54	W
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$

Brake Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	16	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	24	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	61	W
Gate-emitter voltage	V_{GES}		± 20	V
Short circuit ratings	t_{SC}	$V_{GE} = 15\text{ V}$, $V_{CC} = 800\text{ V}$ $T_j = 150\text{ °C}$	10	μs
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$



Vincotech

Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Brake Diode				
Peak repetitive reverse voltage	V_{RRM}		1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	15	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	15	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	33	W
Maximum junction temperature	T_{jmax}		150	°C

Rectifier Diode

Peak repetitive reverse voltage	V_{RRM}		1600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	44	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, $t_p = 10\text{ ms}$ $T_j = 150\text{ °C}$	270	A
Surge current capability	I^2t		370	A ² s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	56	W
Maximum junction temperature	T_{jmax}		150	°C

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage* $t_p = 2\text{ s}$	6000	V
Isolation voltage	V_{isol}	AC Voltage $t_p = 1\text{ min}$	2500	V
Creepage distance			>12,7	mm
Clearance			9,29	mm
Comparative Tracking Index	CTI		≥ 200	

*100 % tested in production



Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		

Inverter Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,0005	25	5,3	5,8	6,3	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		15	25 125 150	1,58	1,87 2,14 2,21	2,07 ⁽¹⁾	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			2	μA
Gate-emitter leakage current	I_{GES}		20	0		25			120	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}	$f = 1 \text{ Mhz}$	0	25		25		890		pF
Reverse transfer capacitance	C_{res}							30		pF
Gate charge	Q_g		20		0	25		120		nC

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)						1,45		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{gon} = 16 \Omega$ $R_{goff} = 16 \Omega$	±15	600	15	25		59,6		ns
Rise time	t_r					125		60,4	ns	
						150		60		
						25		14,8		
Turn-off delay time	$t_{d(off)}$					125		19	ns	
						150		20		
		25		196,6						
Fall time	t_f	125		238,8	ns					
		150		251,6						
		25		79,13						
Turn-on energy (per pulse)	E_{on}	125		105,52	mWs					
		150		110,2						
		25		0,881						
Turn-off energy (per pulse)	E_{off}	125		1,25	mWs					
		150		1,36						
		25		0,883						
						125		1,24		
						150		1,33		



Vincotech

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		
Inverter Diode										
Static										
Forward voltage	V_F			15	25 125 150	1,35	1,76 1,76 1,72	2,05 ⁽¹⁾		V
Reverse leakage current	I_R	$V_r = 1200$ V			25			3,5		μA
Thermal										
Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)					1,78			K/W
Dynamic										
Peak recovery current	I_{RRM}				25 125 150		13,3 16,06 16,46			A
Reverse recovery time	t_{rr}				25 125 150		281,61 433,43 452,84			ns
Recovered charge	Q_r	$di/dt=801$ A/μs $di/dt=1058$ A/μs $di/dt=1081$ A/μs	±15	600	15		1,59 2,75 2,98			μC
Reverse recovered energy	E_{rec}				25 125 150		0,65 1,16 1,25			mWs
Peak rate of fall of recovery current	$(di_r/dt)_{max}$				25 125 150		129,01 108,84 103,64			A/μs



Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GS} [V]	V_{GE} [V]	V_{DS} [V]	V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	

Brake Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$				0,00015	25	5,3	5,8	6,3	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15			8	25 125 150	1,58	1,86 2,14 2,23	2,07 ⁽¹⁾	V
Collector-emitter cut-off current	I_{CES}		0	1200			25			1	μA
Gate-emitter leakage current	I_{GES}		20	0			25			120	nA
Internal gate resistance	r_g								None		Ω
Input capacitance	C_{ies}								490		pF
Reverse transfer capacitance	C_{res}	$f = 1$ Mhz	0	25			25		30		pF

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)							1,57		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$						25 150		60 61,2		ns
Rise time	t_r	$R_{gon} = 32$ Ω $R_{goff} = 32$ Ω					25 150		26,6 26		ns
Turn-off delay time	$t_{d(off)}$		±15	600	8		25 150		178,6 247,2		ns
Fall time	t_f						25 150		68,31 136,63		ns
Turn-on energy (per pulse)	E_{on}	$Q_{tFWD} = 1,01$ μC $Q_{tFWD} = 2,09$ μC					25 150		0,512 0,786		mWs
Turn-off energy (per pulse)	E_{off}						25 150		0,449 0,776		mWs



Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		

Brake Diode

Static

Forward voltage	V_F				7,5	25 125	1,23	1,66 1,62	1,97 ⁽¹⁾	V
Reverse leakage current	I_R	$V_r = 1200$ V				25			27	μA

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						2,12		K/W
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Dynamic

Peak recovery current	I_{RRM}	$di/dt=319$ A/μs $di/dt=279$ A/μs	±15	600	8	25		6,22		A
Reverse recovery time	t_{rr}					150		8,34		
						25		373,84		ns
Recovered charge	Q_r					150		637,21		
						25		1,01		μC
Reverse recovered energy	E_{rec}					150		2,09		
		25		0,457		mWs				
Peak rate of fall of recovery current	$(di_r/dt)_{max}$	150		0,965						
		25		38,9		A/μs				
						150		34,5		

Rectifier Diode

Static

Forward voltage	V_F				13	25 125		1 0,908	1,21 ⁽¹⁾ 1,1 ⁽¹⁾	V
Reverse leakage current	I_R	$V_r = 1600$ V				25			50	μA

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,25		K/W
--	---------------	---------------------------------------	--	--	--	--	--	------	--	-----



Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GS} [V]	V_{GE} [V]	V_{DS} [V]	V_{CE} [V]	T_j [°C]	Min	Typ	Max	

Thermistor

Static

Rated resistance	R					25		22		kΩ
Deviation of R_{100}	$A_{R/R}$	$R_{100} = 1484 \Omega$				100	-5		5	%
Power dissipation	P							5		mW
Power dissipation constant	d					25		1,5		mW/K
B-value	$B_{(25/50)}$	Tol. $\pm 1 \%$						3962		K
B-value	$B_{(25/100)}$	Tol. $\pm 1 \%$						4000		K
Vincotech Thermistor Reference									I	

⁽¹⁾ Value at chip level

⁽²⁾ Only valid with pre-applied Vincotech thermal interface material.

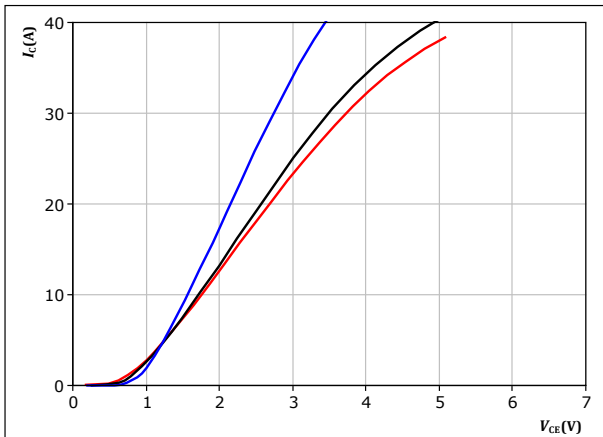


Inverter Switch Characteristics

figure 1. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

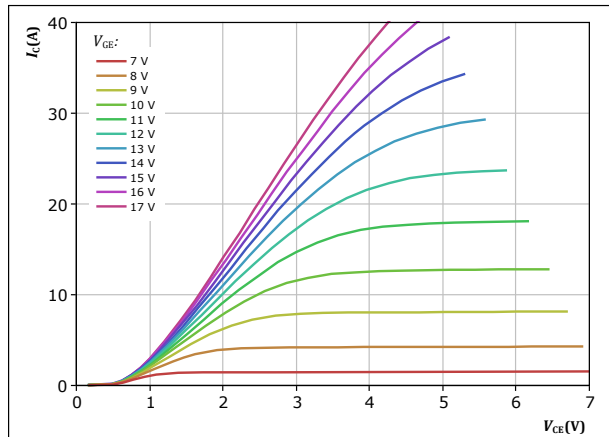


$t_p = 250 \mu s$
 $V_{GE} = 15 V$
 $T_j:$ — 25 °C
— 125 °C
— 150 °C

figure 2. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

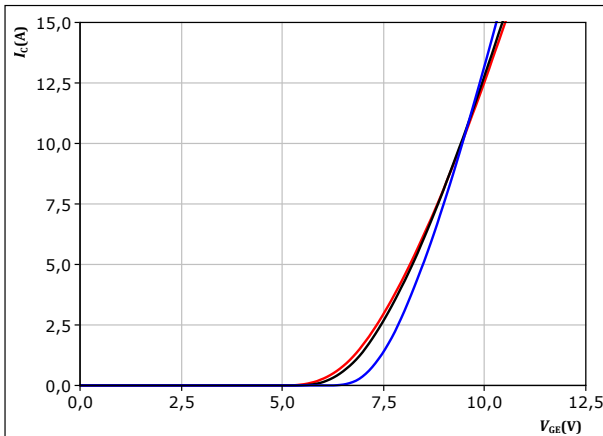


$t_p = 250 \mu s$
 $T_j = 150 \text{ °C}$
 V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$

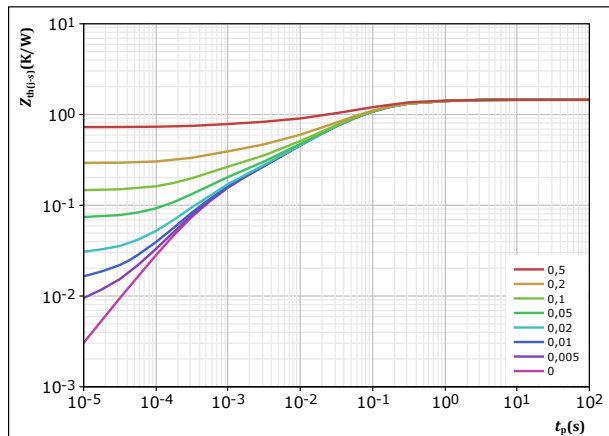


$t_p = 250 \mu s$
 $V_{CE} = 10 V$
 $T_j:$ — 25 °C
— 125 °C
— 150 °C

figure 4. IGBT

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 1,453 \text{ K/W}$
IGBT thermal model values

R (K/W)	τ (s)
1,02E-01	1,17E+00
3,31E-01	1,69E-01
5,63E-01	5,65E-02
2,92E-01	1,03E-02
8,13E-02	1,50E-03
8,53E-02	4,00E-04

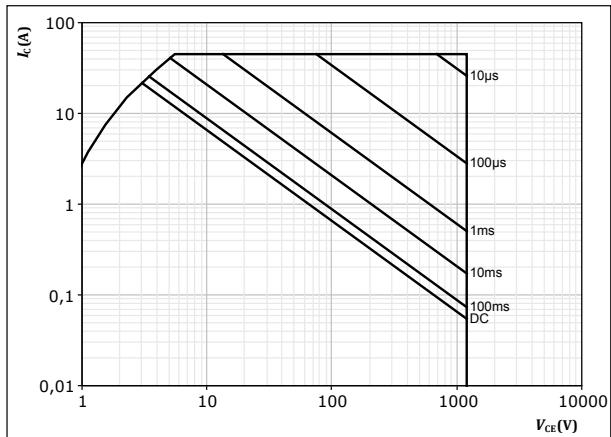


Inverter Switch Characteristics

figure 5. IGBT

Safe operating area

$$I_C = f(V_{CE})$$



$D =$ single pulse

$T_s = 80 \text{ } ^\circ\text{C}$

$V_{GE} = 15 \text{ V}$

$T_j = T_{jmax}$



Inverter Diode Characteristics

figure 6. FWD

Typical forward characteristics

$$I_F = f(V_F)$$

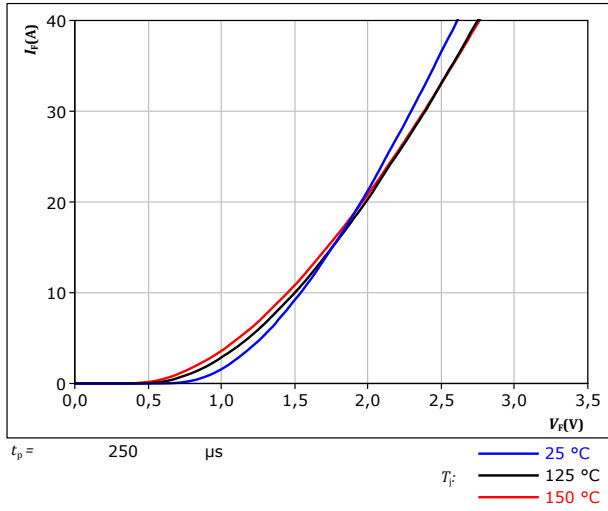
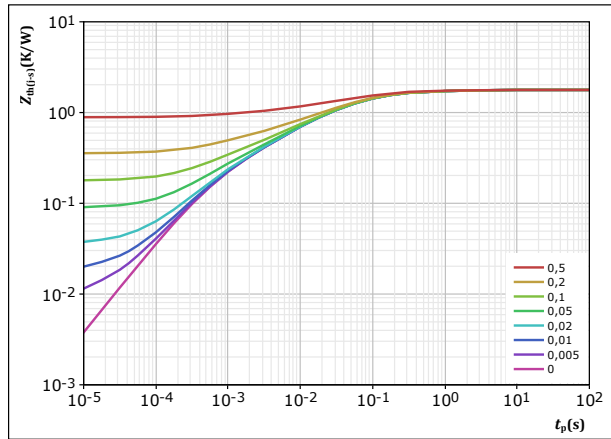


figure 7. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = \frac{t_p}{T}$
 $R_{th(j-s)} = 1,775 \text{ K/W}$
 FWD thermal model values

R (K/W)	τ (s)
8,20E-02	1,96E+00
1,92E-01	2,43E-01
6,68E-01	6,43E-02
4,69E-01	1,43E-02
2,32E-01	2,97E-03
1,33E-01	5,16E-04

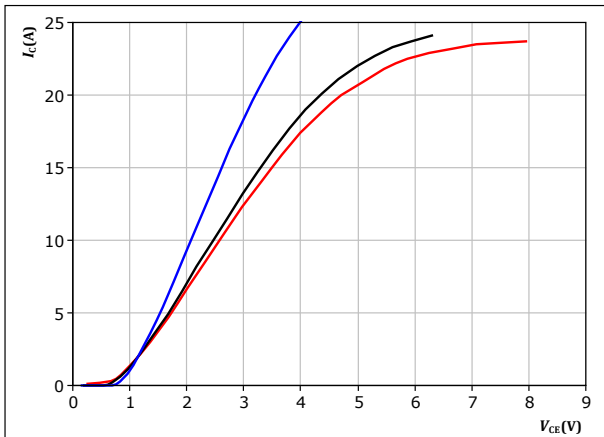


Brake Switch Characteristics

figure 8. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

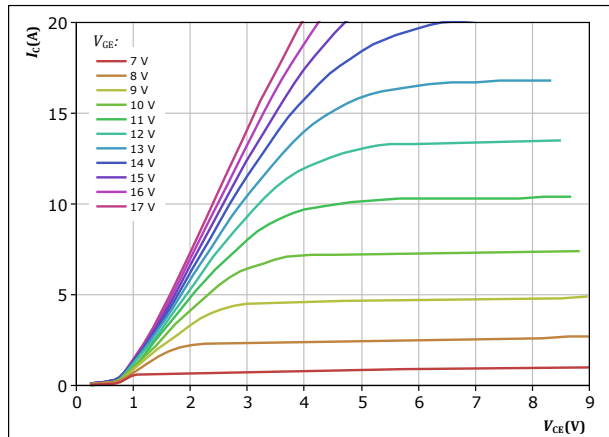


$t_p = 250 \mu s$
 $V_{GE} = 15 V$
 $T_j: 25 \text{ }^\circ C$
 $125 \text{ }^\circ C$
 $150 \text{ }^\circ C$

figure 9. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

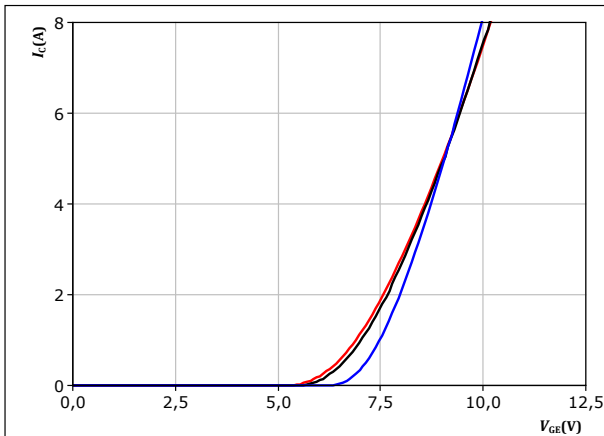


$t_p = 250 \mu s$
 $T_j = 150 \text{ }^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

figure 10. IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$

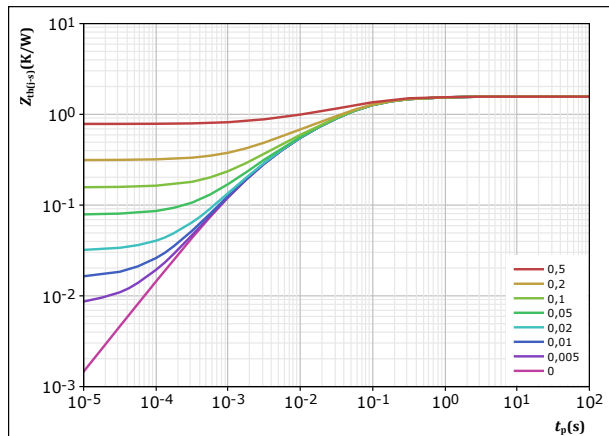


$t_p = 250 \mu s$
 $V_{CE} = 10 V$
 $T_j: 25 \text{ }^\circ C$
 $125 \text{ }^\circ C$
 $150 \text{ }^\circ C$

figure 11. IGBT

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 1,566 K/W$

IGBT thermal model values

R (K/W)	τ (s)
1,42E-01	5,98E-01
6,32E-01	7,71E-02
3,98E-01	2,43E-02
2,86E-01	6,16E-03
1,08E-01	1,44E-03

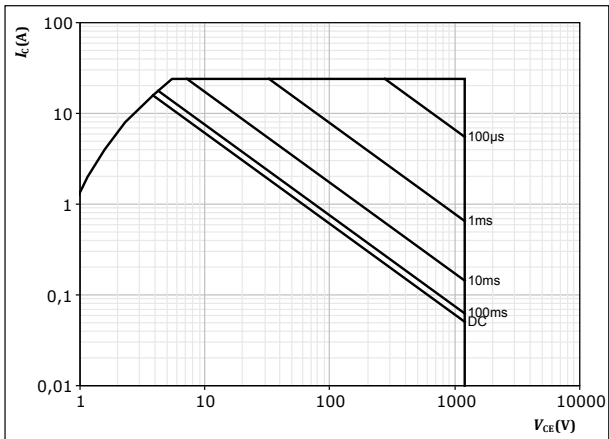


Brake Switch Characteristics

figure 12. IGBT

Safe operating area

$$I_C = f(V_{CE})$$



$D =$ single pulse
 $T_s = 80 \text{ } ^\circ\text{C}$
 $V_{GE} = 15 \text{ V}$
 $T_j = T_{jmax}$

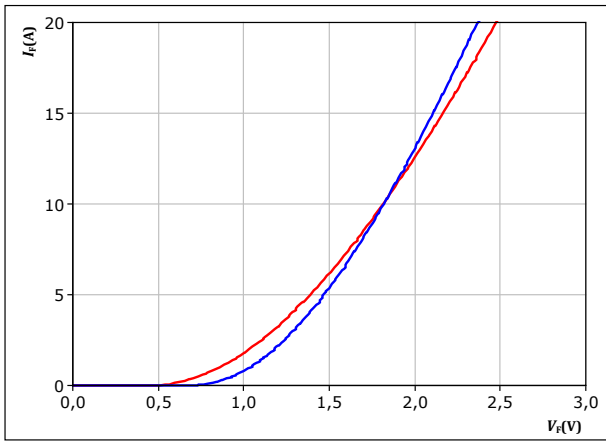


Brake Diode Characteristics

figure 13. FWD

Typical forward characteristics

$$I_F = f(V_F)$$

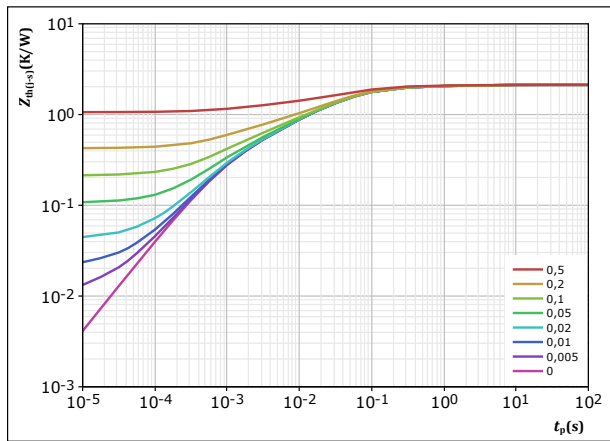


$t_p = 250 \mu s$
 $T_j: \text{ — } 25 \text{ }^\circ\text{C}$
 $\text{ — } 125 \text{ }^\circ\text{C}$

figure 14. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 2,124 \text{ K/W}$
 FWD thermal model values

$R \text{ (K/W)}$	$\tau \text{ (s)}$
7,00E-02	3,23E+00
1,48E-01	4,03E-01
7,34E-01	6,67E-02
5,90E-01	2,04E-02
3,47E-01	4,32E-03
2,36E-01	8,05E-04



Rectifier Diode Characteristics

figure 15. Rectifier

Typical forward characteristics

$$I_F = f(V_F)$$

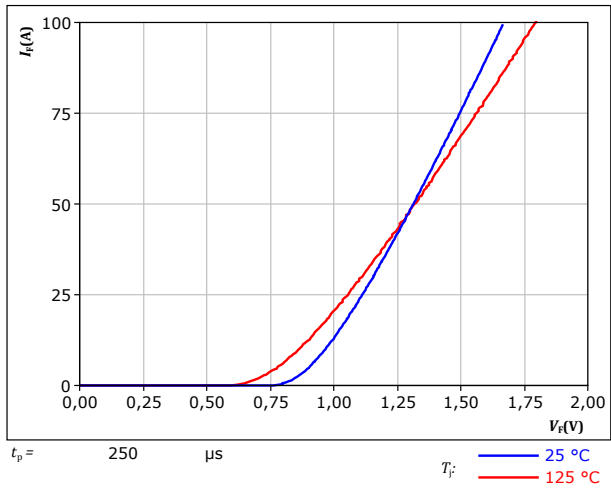
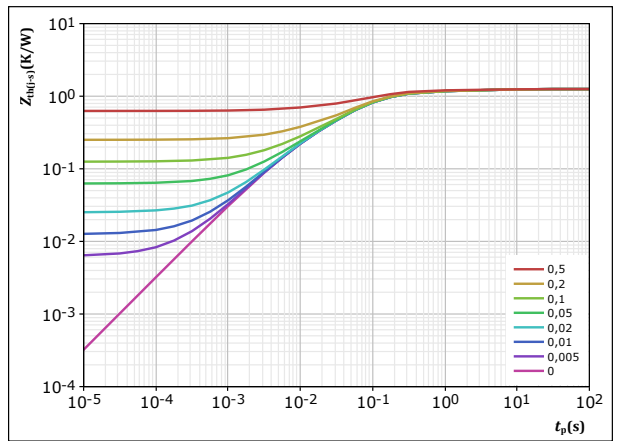


figure 16. Rectifier

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$

$R_{th(j-s)} = 1,254 \text{ K/W}$

Rectifier thermal model values

$R \text{ (K/W)}$	$\tau \text{ (s)}$
8,00E-02	5,22E+00
1,56E-01	4,18E-01
6,95E-01	8,82E-02
2,23E-01	3,07E-02
9,97E-02	5,99E-03

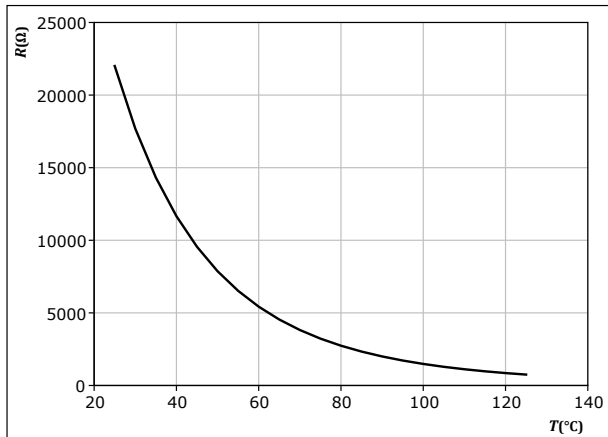


Thermistor Characteristics

figure 17. Thermistor

Typical NTC characteristic as function of temperature

$$R_T = f(T)$$

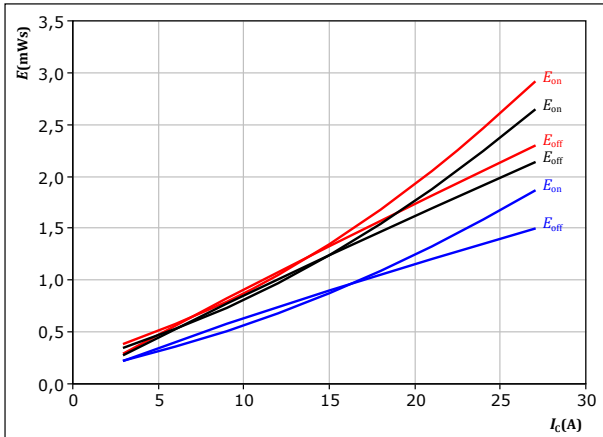




Inverter Switching Characteristics

figure 18. IGBT

Typical switching energy losses as a function of collector current
 $E = f(I_c)$



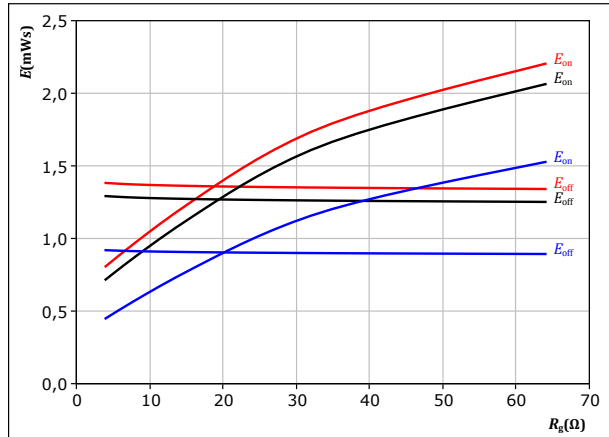
With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 16 \ \Omega$
 $R_{goff} = 16 \ \Omega$

T_j : — 25 °C
 — 125 °C
 — 150 °C

figure 19. IGBT

Typical switching energy losses as a function of gate resistor
 $E = f(R_g)$



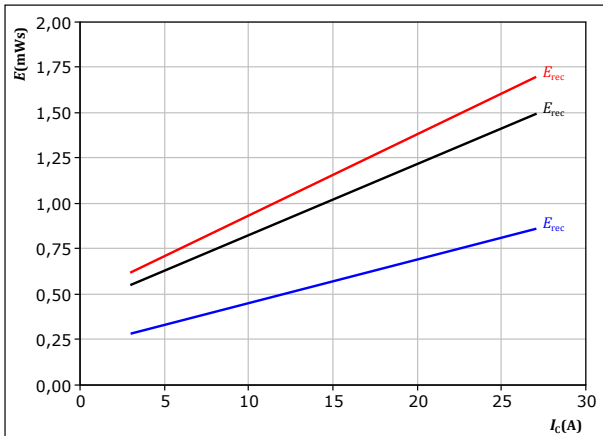
With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 15 \text{ A}$

T_j : — 25 °C
 — 125 °C
 — 150 °C

figure 20. FWD

Typical reverse recovered energy loss as a function of collector current
 $E_{rec} = f(I_c)$



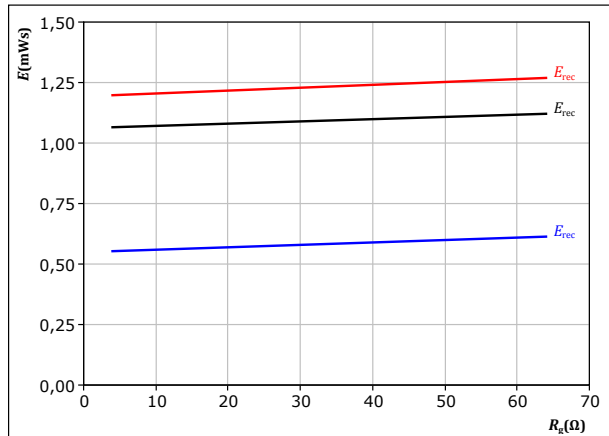
With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 16 \ \Omega$

T_j : — 25 °C
 — 125 °C
 — 150 °C

figure 21. FWD

Typical reverse recovered energy loss as a function of gate resistor
 $E_{rec} = f(R_g)$



With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 15 \text{ A}$

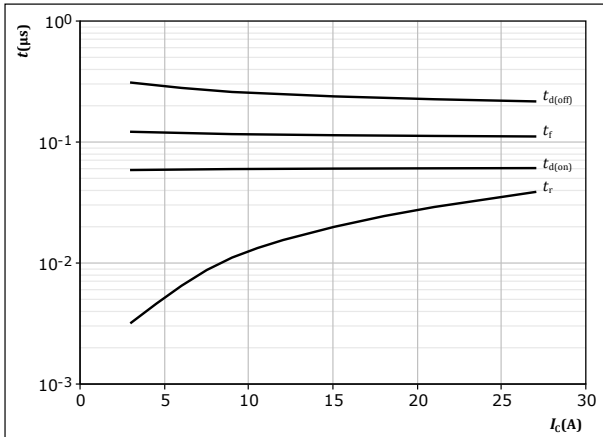
T_j : — 25 °C
 — 125 °C
 — 150 °C



Inverter Switching Characteristics

figure 22. IGBT

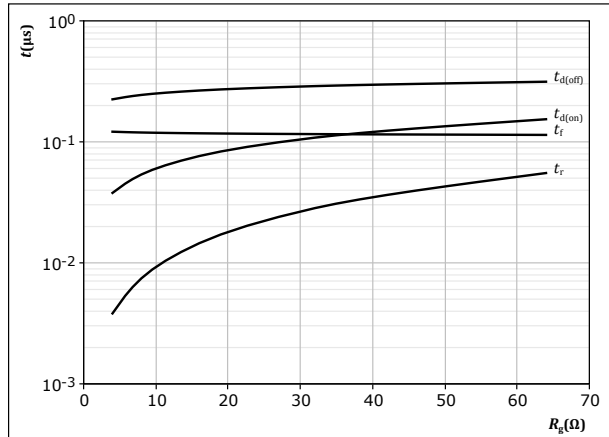
Typical switching times as a function of collector current
 $t = f(I_c)$



With an inductive load at
 $T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{g(on)} = 16 \text{ } \Omega$
 $R_{g(off)} = 16 \text{ } \Omega$

figure 23. IGBT

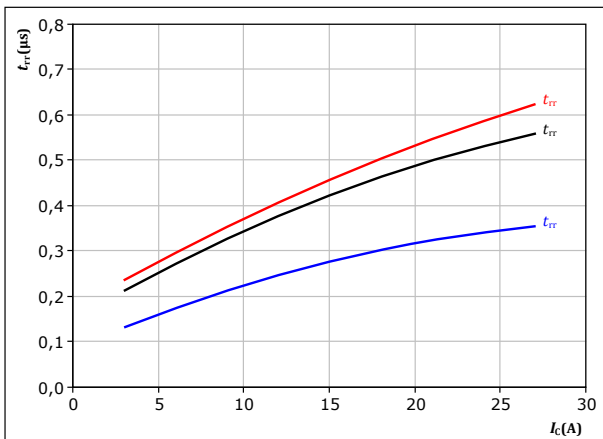
Typical switching times as a function of gate resistor
 $t = f(R_g)$



With an inductive load at
 $T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 15 \text{ A}$

figure 24. FWD

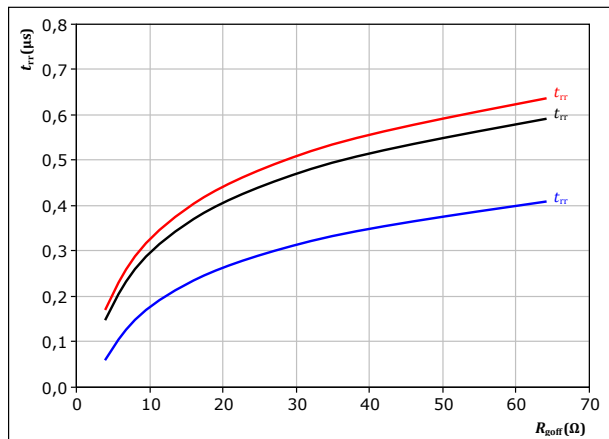
Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_c)$



With an inductive load at
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{g(on)} = 16 \text{ } \Omega$
 $T_j:$ — 25 °C
 — 125 °C
 — 150 °C

figure 25. FWD

Typical reverse recovery time as a function of IGBT turn off gate resistor
 $t_{rr} = f(R_{g(off)})$



With an inductive load at
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 15 \text{ A}$
 $T_j:$ — 25 °C
 — 125 °C
 — 150 °C

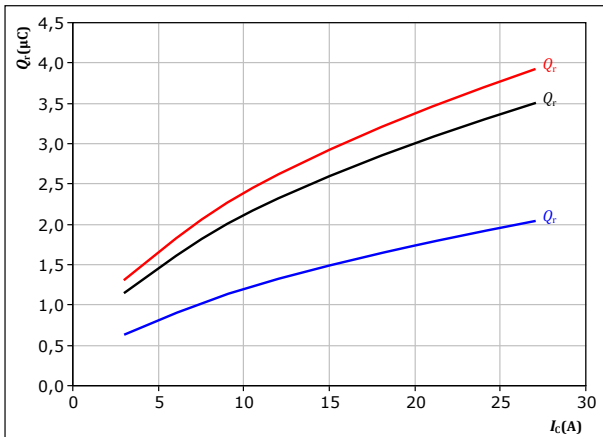


Inverter Switching Characteristics

figure 26. FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

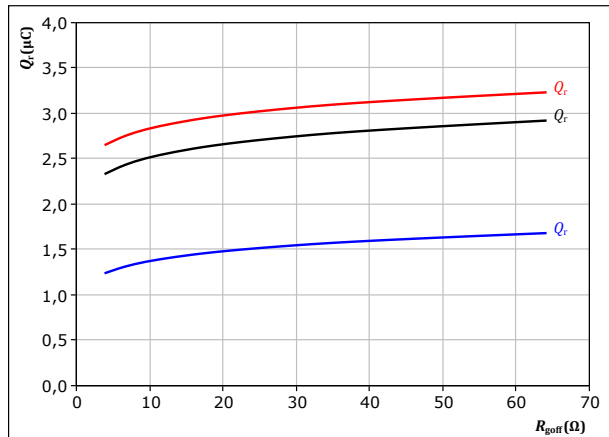
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{goff} = 16 \ \Omega$

T_j : — 25 °C
— 125 °C
— 150 °C

figure 27. FWD

Typical recovered charge as a function of turn off gate resistor

$$Q_r = f(R_{goff})$$



With an inductive load at

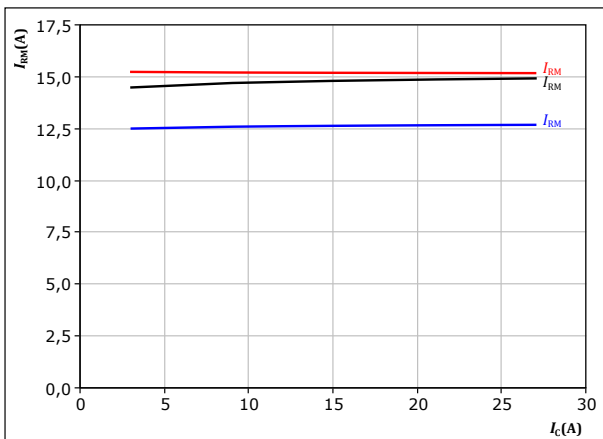
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 15 \text{ A}$

T_j : — 25 °C
— 125 °C
— 150 °C

figure 28. FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

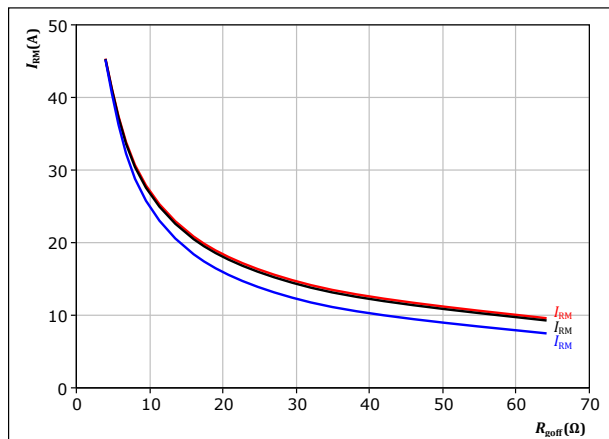
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{goff} = 16 \ \Omega$

T_j : — 25 °C
— 125 °C
— 150 °C

figure 29. FWD

Typical peak reverse recovery current as a function of turn off gate resistor

$$I_{RM} = f(R_{goff})$$



With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 15 \text{ A}$

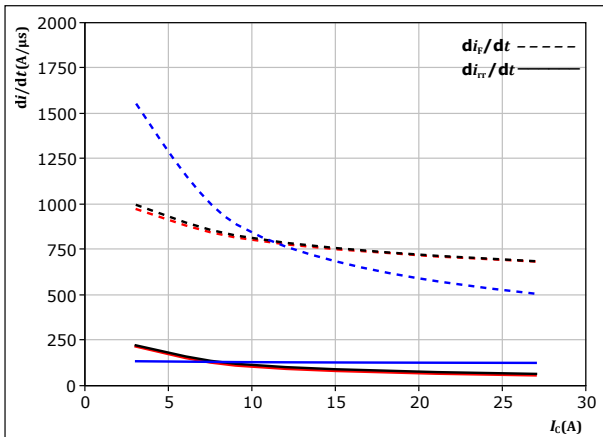
T_j : — 25 °C
— 125 °C
— 150 °C



Inverter Switching Characteristics

figure 30. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_{rr}/dt = f(I_c)$



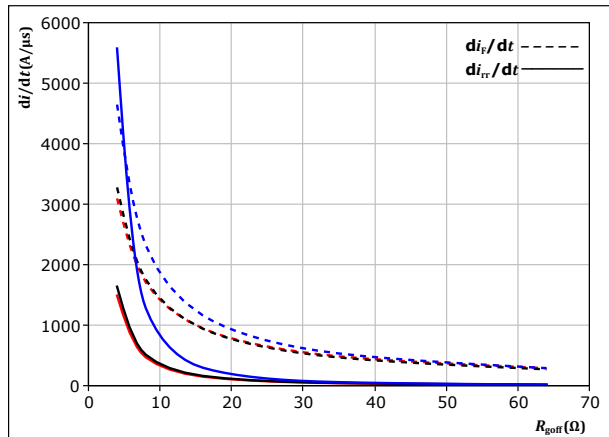
With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{goff} = 16 \ \Omega$

T_j :
— 25 °C
— 125 °C
— 150 °C

figure 31. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor
 $di_f/dt, di_{rr}/dt = f(R_{goff})$



With an inductive load at

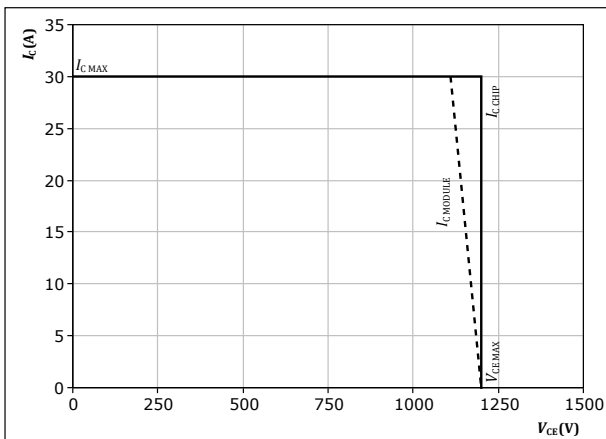
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 15 \text{ A}$

T_j :
— 25 °C
— 125 °C
— 150 °C

figure 32. IGBT

Reverse bias safe operating area

$I_c = f(V_{CE})$



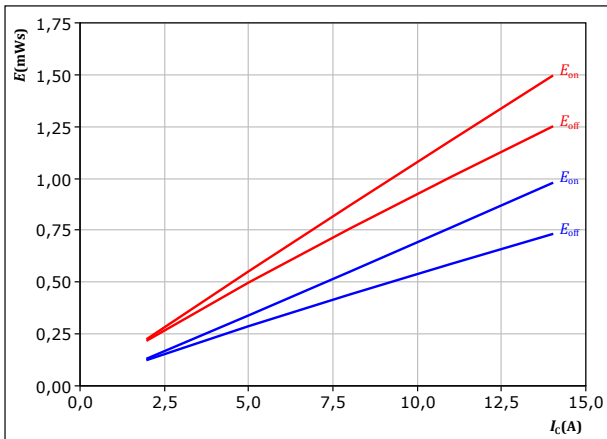
At $T_j = 150 \text{ °C}$
 $R_{goff} = 16 \ \Omega$
 $R_{goff} = 16 \ \Omega$



Brake Switching Characteristics

figure 33. IGBT

Typical switching energy losses as a function of collector current
 $E = f(I_c)$



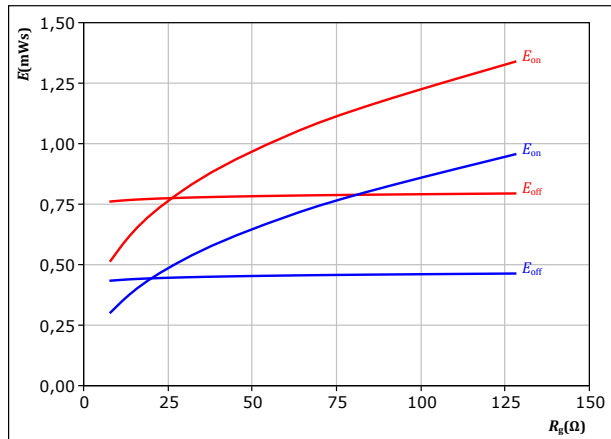
With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 32$ Ω
 $R_{goff} = 32$ Ω

T_j : — 25 °C
 — 150 °C

figure 34. IGBT

Typical switching energy losses as a function of gate resistor
 $E = f(R_g)$



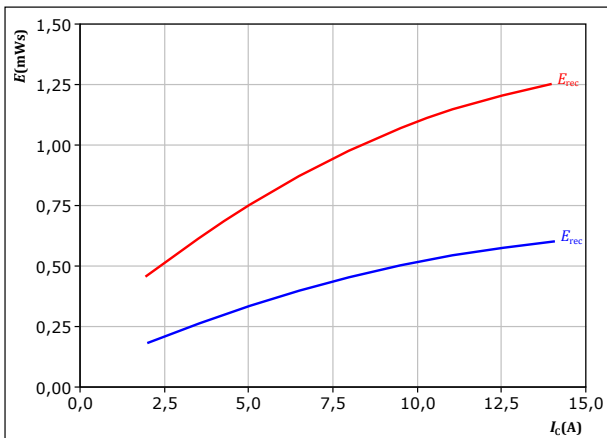
With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_c = 8$ A

T_j : — 25 °C
 — 150 °C

figure 35. FWD

Typical reverse recovered energy loss as a function of collector current
 $E_{rec} = f(I_c)$



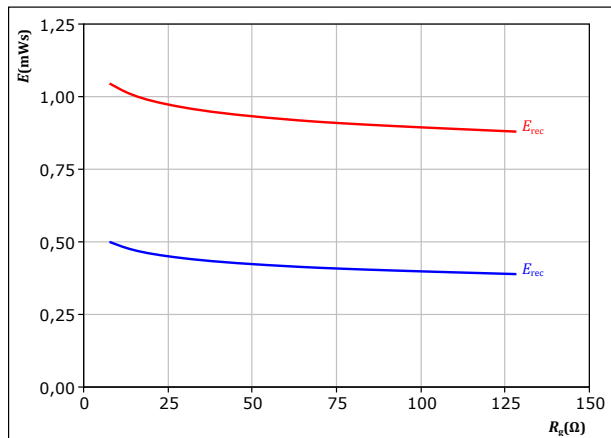
With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 32$ Ω

T_j : — 25 °C
 — 150 °C

figure 36. FWD

Typical reverse recovered energy loss as a function of gate resistor
 $E_{rec} = f(R_g)$



With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_c = 8$ A

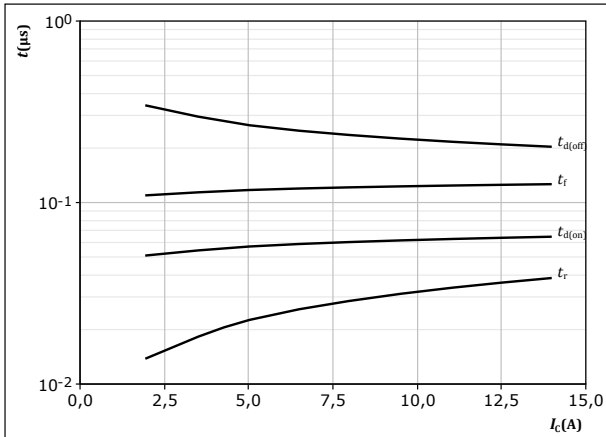
T_j : — 25 °C
 — 150 °C



Brake Switching Characteristics

figure 37. IGBT

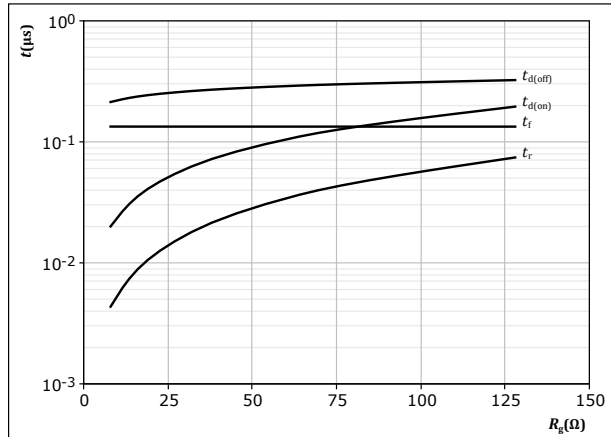
Typical switching times as a function of collector current
 $t = f(I_c)$



With an inductive load at
 $T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 32 \text{ } \Omega$
 $R_{goff} = 32 \text{ } \Omega$

figure 38. IGBT

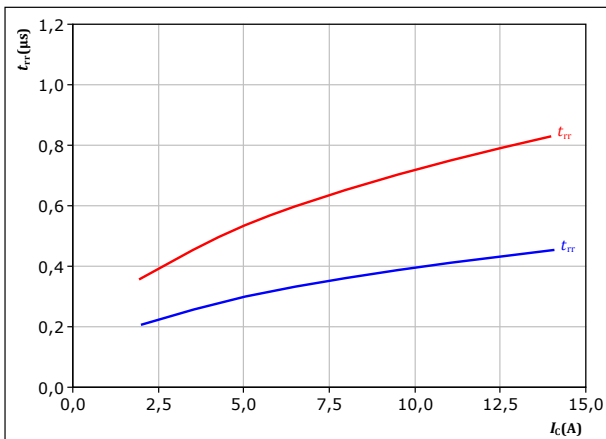
Typical switching times as a function of gate resistor
 $t = f(R_g)$



With an inductive load at
 $T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 8 \text{ A}$

figure 39. FWD

Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_c)$

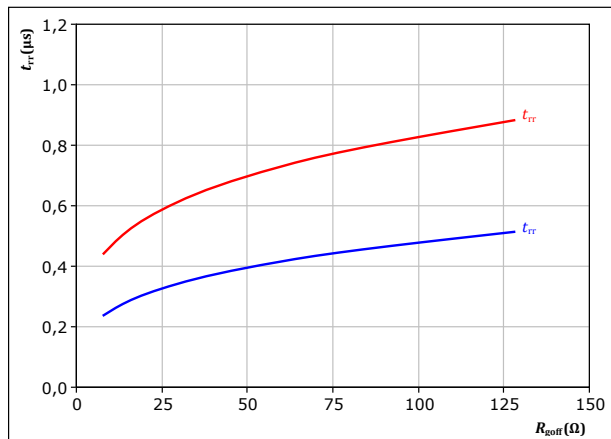


With an inductive load at
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 32 \text{ } \Omega$

T_j : — 25 °C
 — 150 °C

figure 40. FWD

Typical reverse recovery time as a function of IGBT turn off gate resistor
 $t_{rr} = f(R_{goff})$



With an inductive load at
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 8 \text{ A}$

T_j : — 25 °C
 — 150 °C

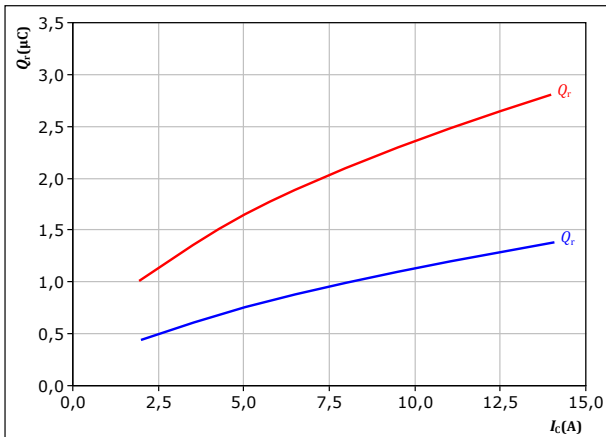


Brake Switching Characteristics

figure 41. FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

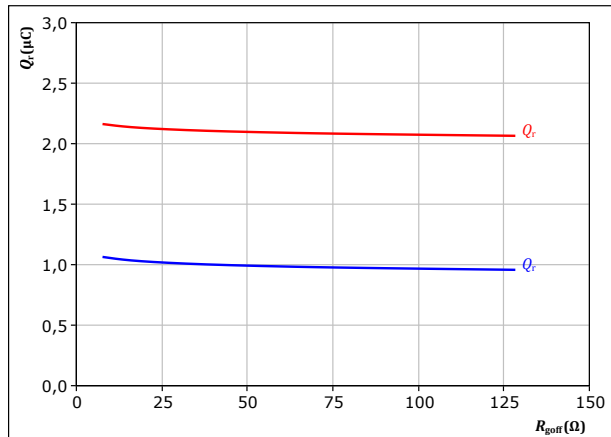
$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{goff} = 32$ Ω

T_j : — 25 °C
— 150 °C

figure 42. FWD

Typical recovered charge as a function of turn off gate resistor

$$Q_r = f(R_{goff})$$



With an inductive load at

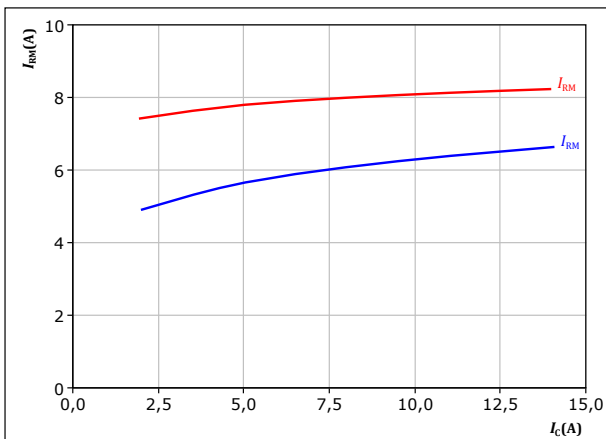
$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_c = 8$ A

T_j : — 25 °C
— 150 °C

figure 43. FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

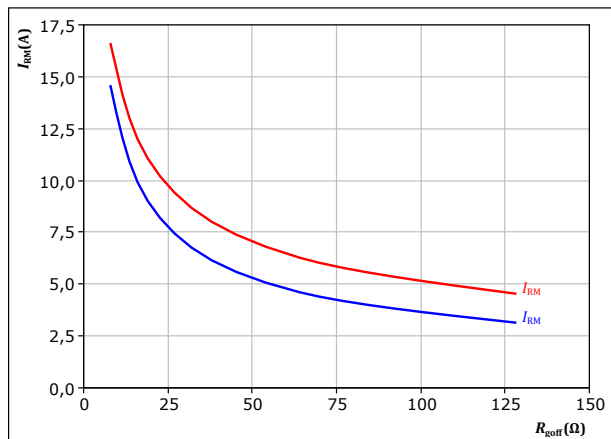
$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{goff} = 32$ Ω

T_j : — 25 °C
— 150 °C

figure 44. FWD

Typical peak reverse recovery current as a function of turn off gate resistor

$$I_{RM} = f(R_{goff})$$



With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_c = 8$ A

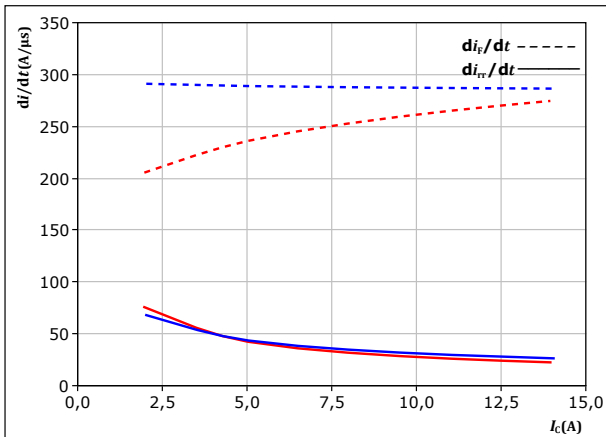
T_j : — 25 °C
— 150 °C



Brake Switching Characteristics

figure 45. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_r/dt = f(I_C)$



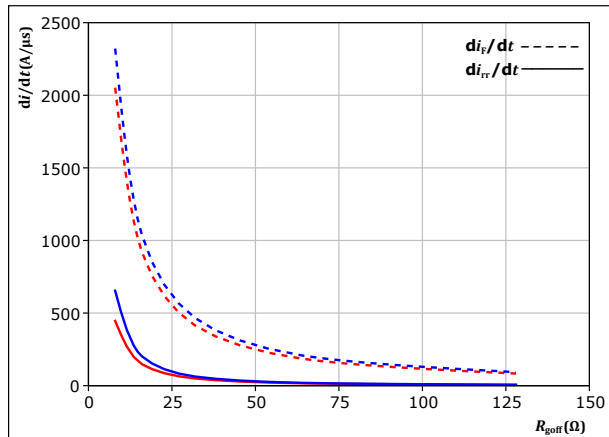
With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{goff} = 32 \ \Omega$

T_j : — 25 °C
 — 150 °C

figure 46. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor
 $di_f/dt, di_r/dt = f(R_{goff})$



With an inductive load at

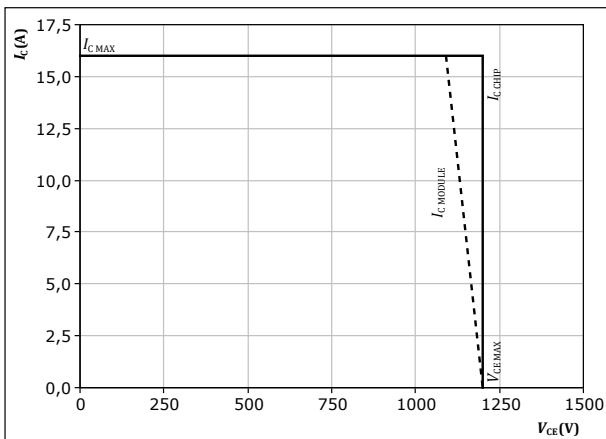
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 8 \text{ A}$

T_j : — 25 °C
 — 150 °C

figure 47. IGBT

Reverse bias safe operating area

$I_C = f(V_{CE})$



At $T_j = 150 \text{ °C}$
 $R_{goff} = 32 \ \Omega$
 $R_{goff} = 32 \ \Omega$



Switching Definitions

figure 48. IGBT

Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff} (t_{Eoff} = integrating time for E_{off})

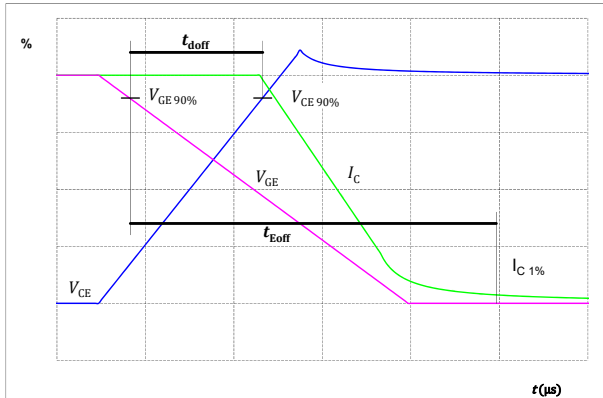


figure 49. IGBT

Turn-on Switching Waveforms & definition of t_{don} , t_{Eon} (t_{Eon} = integrating time for E_{on})



figure 50. IGBT

Turn-off Switching Waveforms & definition of t_f

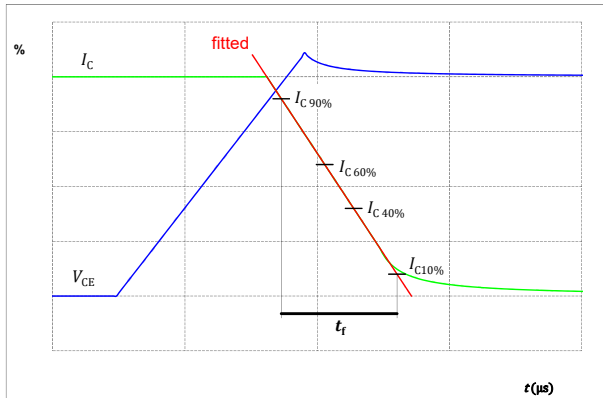
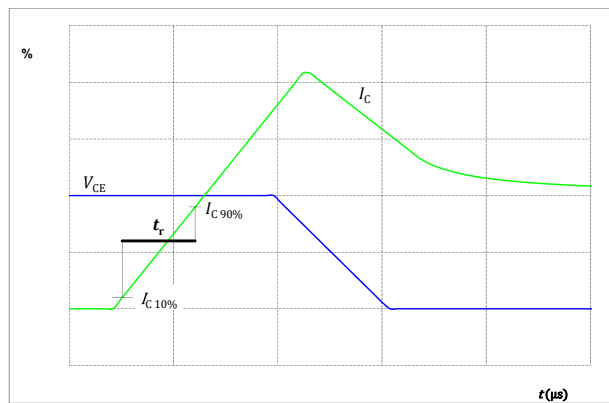


figure 51. IGBT

Turn-on Switching Waveforms & definition of t_r





Switching Definitions

figure 52. FWD

Turn-off Switching Waveforms & definition of t_{rr}

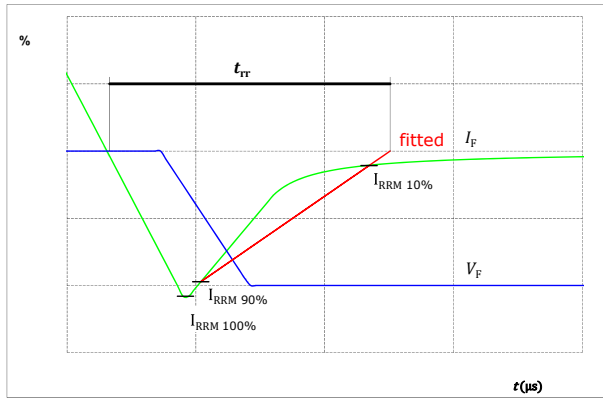
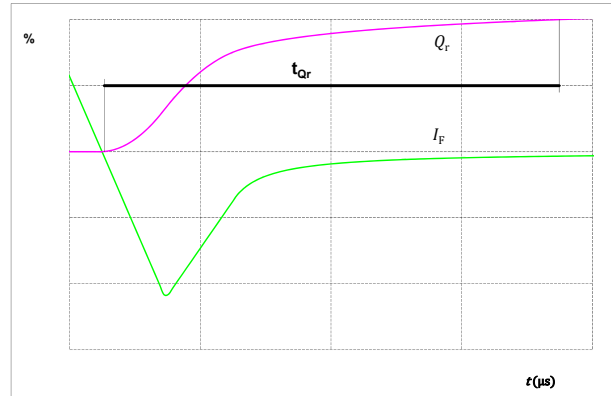



figure 53. FWD

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)

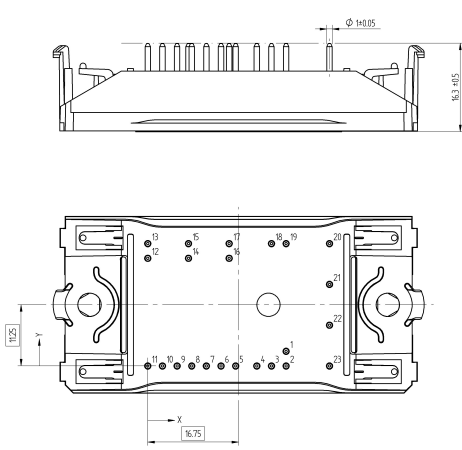




Ordering Code	
Version	Ordering Code
Without thermal paste	V23990-P840-A58-PM
With thermal paste (5,2 W/mK, PTM6000HV)	V23990-P840-A58-/7/-PM
With thermal paste (3,4 W/mK, PSX-P7)	V23990-P840-A58-/3/-PM

Marking							
	Text	VIN VIN	Date code WWYY	Type&Ver TTTTTTTV	UL UL	Lot LLLLL	Serial SSSS
	Datamatrix	Type&Ver	Lot number	Serial	Date code		
		TTTTTTTV	LLLLL	SSSS	WWYY		

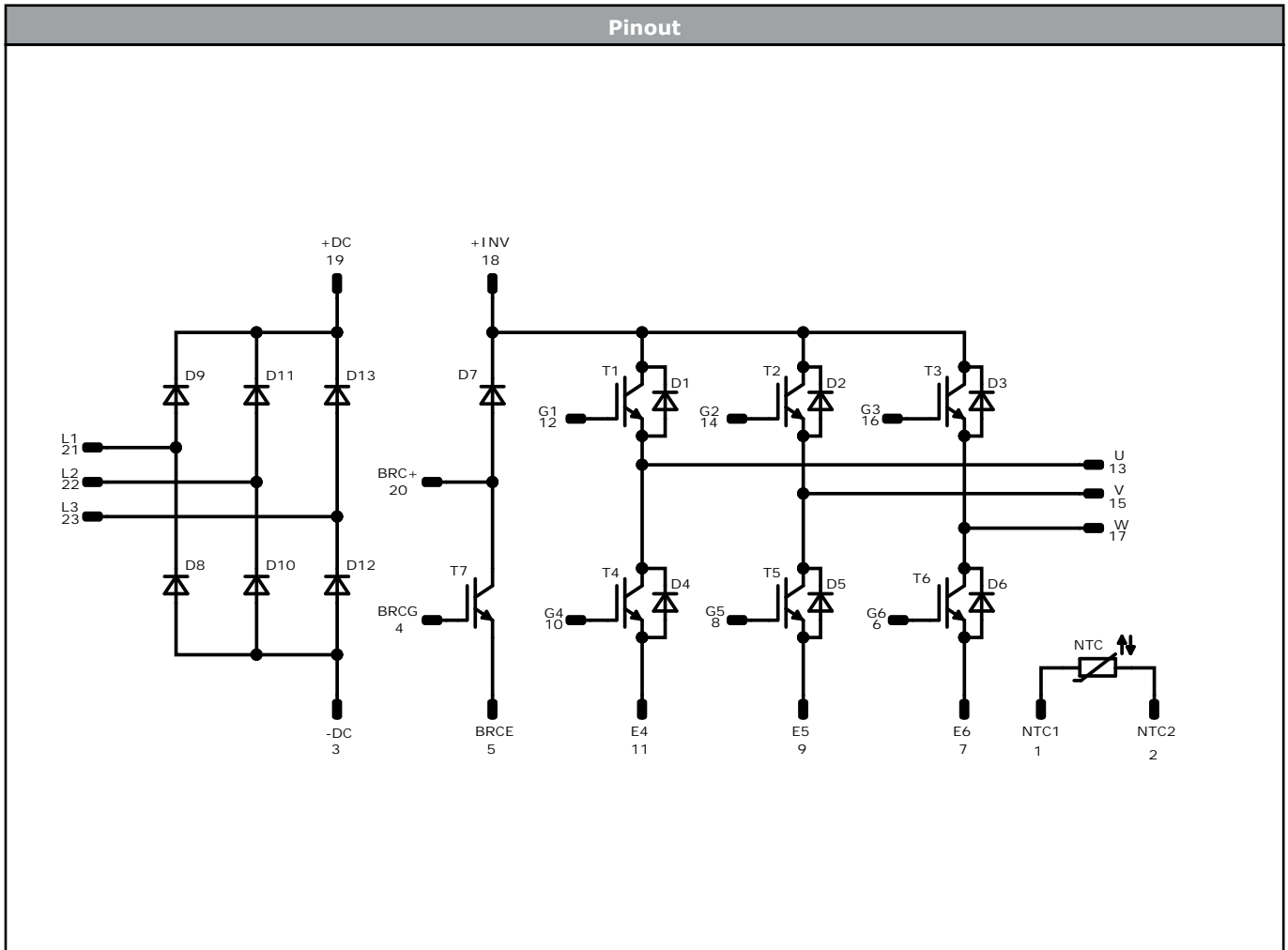
Pin table [mm]				Outline
Pin	X	Y	Function	
1	25,5	2,7	NTC1	
2	25,5	0	NTC2	
3	22,8	0	-DC	
4	20,1	0	BRCG	
5	16,2	0	BRCE	
6	13,5	0	G6	
7	10,8	0	E6	
8	8,1	0	G5	
9	5,4	0	E5	
10	2,7	0	G4	
11	0	0	E4	
12	0	19,8	G1	
13	0	22,5	U	
14	7,5	19,8	G2	
15	7,5	22,5	V	
16	15	19,8	G3	
17	15	22,5	W	
18	22,8	22,5	+INV	
19	25,5	22,5	+DC	
20	33,5	22,5	BRC+	
21	33,5	15	L1	
22	33,5	7,5	L2	
23	33,5	0	L3	



Tolerance of pin positions: 30.5mm at the end of pins
Dimension of coordinate axis is only offset without tolerance



Vincotech



Identification					
ID	Component	Voltage	Current	Function	Comment
T4, T1, T5, T2, T6, T3	IGBT	1200 V	15 A	Inverter Switch	
D1, D4, D2, D5, D3, D6	FWD	1200 V	15 A	Inverter Diode	
T7	IGBT	1200 V	8 A	Brake Switch	
D7	FWD	1200 V	7,5 A	Brake Diode	
D8, D9, D10, D11, D12, D13	Rectifier	1600 V	35 A	Rectifier Diode	
NTC	Thermistor			Thermistor	




Packaging instruction				
Standard packaging quantity (SPQ) 135	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for <i>flow 0</i> packages see vincotech.com website.

Package data
Package data for <i>flow 0</i> packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website. 

Document No.:	Date:	Modification:	Pages
V23990-P840-A58-PM-D8-14	30 Sep. 2021	New Datasheet format, module is unchanged Introduce Rth values with PSX-P7 TIM Separate datasheet for pressfit pin version	

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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.